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Interactive Presentations:

Track / Room	08:30 - 10:00	10:00 - 10:30	10:00 - 11:00	11:00 - 12:30	12:30 - 14:30	13:50 - 14:20	14:30 - 16:00	16:00 - 16:30
Track 1 Room 1	5.1 Special Day on "Embedded Meets Hyperscale and HPC" Session: Heterogeneous Computing in the Datacenter and in HPC			6.1 Special Day on "Embedded Meets Hyperscale and HPC" Session: Near-memory computing			7.1 Special Day on "Embedded Meets Hyperscale and HPC" Session: Tools and Runtime Systems	
Track 2 Room 2	5.2 Improving Formal Verification and Applications to GPUs and High-Level Synthesis			6.2 Special Session: 3D Sensor - Hardware to Application			7.2 Accelerators using novel memory technologies	
Track 3 Room 3	5.3 EU Projects			6.3 When Approximation Meets Dependability			7.3 CPU and GPU microarchitecture dependability	
Track 4 Room 4	5.4 Emerging technologies for better NoCs			6.4 Hardware support for microarchitecture performance			7.4 Low Power Design: From Highly-Optimized Power Delivery Networks to CNN Accelerators	
Track 5 Room 5	5.5 Hardware Obfuscation			6.5 System Level Security			7.5 Reliable and Persistent: From Cache to File system	
Track 6 Room 6	5.6 Energy efficiency in IoT - Edge to			6.6 Intelligent Wearable and Implantable			7.6 Optimization of Smart Energy Systems	

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