



Published on DATE 2019 (<https://past.date-conference.com>)

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Friday Workshops

DATE's Friday's Workshops initiative started in 2003 fostering a number of workshops on different topics and attracting an audience of both researchers and designers.

The event offers a unique opportunity to present and discuss new and in-progress outcomes, as well as experiences and visions. The topics have been changing during the years, following the trend on current and emerging issues in design, test, software and EDA, taking the floor after the regular conference programme running throughout the week.

The Friday's Workshops programme for **DATE 2019** is rich and includes seven workshop themes. These cover innovative ideas ranging from design automation to emerging memory solutions, from optical interconnect systems to adaptive and heterogeneous computing.

Friday's Workshops attendees should choose in advance one workshop among W01, W02, W03, W04, W05, W06, W07, W08, W09 or W10. The workshops run from 08:30 until 17:00. The individual timetables for each workshop may vary, and for the detailed version of the workshop programme, visit the workshop webpages.

Registration for the workshops is open from 7:30. Welcome Refreshments are served from 7:30 - 8:30.

For more information, please contact:

Friday Workshops Co-Chairs

Francisco Cazorla, Barcelona Supercomputing Center and IIIA-CSIC, ES

friday-workshops @ date-conference [dot] com

Michael Glaß, Ulm University, DE

friday-workshops @ date-conference [dot] com

Session ID	Title	Time	Location / Room	Details
W01	The 5th International Workshop on Optical/Photonic Interconnects for Computing Systems (OPTICS)	08:30-17:30	Room 3	Read More
W02	Recent Trends in Memristor Science & Technology: the journey from single memristor device towards 100 trillion synapses of brain	08:30-17:30	Room 2	Read More
W03	DATE Workshop on Autonomous Systems Design (ASD2019)	08:30-17:30	Room 1	Read More
W04	6th Workshop on Design Automation for Understanding Hardware Designs (DUHDE6)	08:30-17:30	Room 9	Read More
W05	AxC: 4th Workshop on Approximate Computing	08:30-17:00	Room 5	Read More
W06	2nd International Workshop on Embedded Software for the Industrial IoT (ESIIT 2019)	08:45-16:15	Room 6	Read More
W07	Workshop on Machine Learning for CAD	08:30-17:30	Room 7	Read More
W08	Grand Challenges and Research Tools for Quantum Computing	08:30-17:00	Room 10	Read More
W09	Quo vadis, Logic Synthesis?	08:30-17:30	Room 8	Read More
W10	Workshop on Open-Source Design Automation for FPGAs - OSDA	08:45-17:30	Room 4	Read More

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