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W05 AxC: 4th Workshop on Approximate Computing

Agenda

Time	Label	Session
07:30	W05.1	Registration Desk opens
08:30	W05.2	Opening Session
09:00	W05.3	Keynote 1 Keynote "Transprecision Computing for Energy-Efficiency" by dr. Cristiano Malossi from IBM Research of Zurich
10:00	W05.4	Coffee Break
10:30	W05.5	Technical Session 1 Resilience-based Mapping of Deep Neural Network Operations to Approximate Computing Units <i>Christoph Schorn (Robert Bosch GmbH), Matthias Roth (Esslingen University of Applied Sciences), Andre Guntoro (Robert Bosch GmbH) and Gerd Ascheid (RWTH Aachen University)</i> Noise Budgeting in Multiple-Kernel Word-Length Optimization <i>Van-Phu Ha (University of Rennes 1), Tomofumi Yuki (University of Rennes 1) and Olivier Sentieys (University of Rennes 1)</i> Reliability Evaluation of Mixed-Precision Architectures <i>Paolo Rech (UFRGS), Fernando Fernandes dos Santos (UFRGS), and Daniel Oliveira (UFRGS)</i> Approximate Computing of Transcendental Functions Applied to Artificial Neural Networks <i>Xin Fan (IDS, RWTH Aachen Univ), Arthur Ruder (IDS, RWTH Aachen Univ), Cecilia Höfler (IDS, RWTH Aachen Univ), and Tobias Gemmeke (IDS, RWTH Aachen Univ)</i>
12:00	W05.6	Lunch break
13:00	W05.7	Invited Talk Approximate Computing in HPC: building from ground up <i>Alessandro Cilardo (Università degli Studi di Napoli Federico II)</i>
13:30	W05.8	Keynote 2 Keynote : Re-Engineering Computing with Neuro-Inspired Learning: Devices, Circuits, Systems. and Approximations" by prof. Kaushik Roy from School of Electrical and Computer Engineering of Purdue University.
14:30	W05.9	Coffee Break
15:00	W05.10	Technical Session 2 Targeting Approximation through Data Lifetime: A Quest for Optimization Metrics <i>Alessandro Savino (Politecnico di Torino), Michele Portolan (Univ. Grenoble Alpes, TIMA), Stefano Di Carlo (Politecnico di Torino) and Regis Leveugle (Univ. Grenoble Alpes, TIMA)</i> Jump Search: A Fast Technique for the Synthesis of Approximate Circuits <i>Linus Witschen (Paderborn University), Hassan Ghasemzadeh Mohammadi (Paderborn University), Matthias Artmann (Paderborn University) and Marco Platzner (Paderborn University)</i> An Approximate Communication Technique for Energy Efficient Networks on Chip <i>Giuseppe Ascia (University of Catania), Vincenzo Catania (University of Catania), John Jose (Indian Institute of Technology Guwahati), Salvatore Monteleone (University of Catania), Maurizio Palesi (University of Catania) and Davide Patti (University of Catania)</i> Adjustable Precision Computing Using Redundant Arithmetic <i>Ali Skaf (TIMA), Mona Ezzadeen (TIMA), Mounir Benabdenbi (TIMA) and Laurent Fesquet (TIMA)</i> Towards One Million Component Library of Approximate Circuits <i>Lukas Sekanina (Brno University of Technology), Zdenek Vasicek (Brno University of Technology) and Vojtech Mrazek (Brno University of Technology)</i> Exploiting Approximate Computing to Increase System Lifetime <i>E. Sanchez (Politecnico di Torino), P. Bernardi (Politecnico di Torino) and W. J. Perez-Holguin (Universidad Pedagógica y Tecnológica de Colombia)</i> Approximate Computing for Sizing Hidden Layer in CNN <i>Stefano Marrone (University of Naples Federico II) and Carlo Sansone (University of Naples Federico II)</i>

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