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Session Label	Session Title	Time	Location	Details
5.1	Special Day on "Embedded Meets Hyperscale and HPC" Session: Heterogeneous Computing in the Datacenter and in HPC	08:30 - 10:00	Room 1	Read More
5.2	Improving Formal Verification and Applications to GPUs and High-Level Synthesis	08:30 - 10:00	Room 2	Read More
5.3	EU Projects	08:30 - 10:00	Room 3	Read More
5.4	Emerging technologies for better NoCs	08:30 - 10:00	Room 4	Read More
5.5	Hardware Obfuscation	08:30 - 10:00	Room 5	Read More
5.6	Energy efficiency in IoT - Edge to Cloud	08:30 - 10:00	Room 6	Read More
5.7	Data-driven Acceleration	08:30 - 10:00	Room 7	Read More
5.8	Special Session: The ARAMIS II Project - Efficient Use of Multicore for safety-critical Applications	08:30 - 10:00	Exh. Theatre	Read More
IP2	Interactive Presentations	10:00 - 10:30	Poster Area	Read More
6.1	Special Day on "Embedded Meets Hyperscale and HPC" Session: Near-memory computing	11:00 - 12:30	Room 1	Read More
6.2	Special Session: 3D Sensor - Hardware to Application	11:00 - 12:30	Room 2	Read More
6.3	When Approximation Meets Dependability	11:00 - 12:30	Room 3	Read More
6.4	Hardware support for microarchitecture performance	11:00 - 12:30	Room 4	Read More
6.5	System Level Security	11:00 - 12:30	Room 5	Read More
6.6	Intelligent Wearable and Implantable Sensors for Augmented Living	11:00 - 12:30	Room 6	Read More
6.7	How Secure and Verified is your Cyber-Physical System?	11:00 - 12:30	Room 7	Read More
7.0	LUNCH TIME KEYNOTE SESSION	13:50 - 14:20	Room 1	Read More
7.1	Special Day on "Embedded Meets Hyperscale and HPC" Session: Tools and Runtime Systems	14:30 - 16:00	Room 1	Read More
7.2	Accelerators using novel memory technologies	14:30 - 16:00	Room 2	Read More
7.3	CPU and GPU microarchitecture dependability	14:30 - 16:00	Room 3	Read More
7.4	Low Power Design: From Highly-Optimized Power Delivery Networks to CNN Accelerators	14:30 - 16:00	Room 4	Read More
7.5	Reliable and Persistent: From Cache to File system	14:30 - 16:00	Room 5	Read More
7.6	Optimization of Smart Energy Systems	14:30 - 16:00	Room 6	Read More
7.7	Toward Correct and Secure Embedded Systems	14:30 - 16:00	Room 7	Read More
IP3	Interactive Presentations	16:00 - 16:30	Poster Area	Read More
8.1	Special Day on "Embedded Meets Hyperscale and HPC" Panel: What can HPC and hyperscale learn from embedded computing	17:00 - 18:30	Room 1	Read More
8.2	Special Session: Innovative methods for verifying Systems-on-Chip: digital, mixed-signal, security and software	17:00 - 18:30	Room 2	Read More
8.3	Test Preparation and Generation	17:00 - 18:30	Room 3	Read More
8.4	Applications of Reconfigurable Computing	17:00 - 18:30	Room 4	Read More
8.5	Don't Forget the Memory	17:00 - 18:30	Room 5	Read More
8.6	Robotics and Industry 4.0	17:00 - 18:30	Room 6	Read More
8.7	Embedded hardware architectures for deep neural networks	17:00 - 18:30	Room 7	Read More

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