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Session Label	Session Title	Time	Location	Details
2.1	Executive Session 1: Panel "Life After CMOS"	11:30 - 13:00	Room 1	Read More
2.2	Physical Attacks	11:30 - 13:00	Room 2	Read More
2.3	Special Session: Circuit design and design automation for flexible electronics	11:30 - 13:00	Room 3	Read More
2.4	Temperature and Variability Driven Modeling and Runtime Management	11:30 - 13:00	Room 4	Read More
2.5	Solutions for reliability and security of mixed-signal circuits	11:30 - 13:00	Room 5	Read More
2.6	Computational and resource-efficiency in quantum and approximate computing	11:30 - 13:00	Room 6	Read More
2.7	Analysis and optimization techniques for neural networks	11:30 - 13:00	Room 7	Read More
3.0	LUNCH TIME KEYNOTE SESSION	13:50 - 14:20	Room 1	Read More
3.1	Executive Session 2: Panel "Semiconductor IP, Surfing the Next Big Wave"	14:30 - 16:00	Room 1	Read More
3.2	Special Session: Smart Resource Management and Design Space Exploration for Heterogenous Processors	14:30 - 16:00	Room 2	Read More
3.3	Methods and Characterisation techniques for Reliability	14:30 - 16:00	Room 3	Read More
3.4	Physical Design, Extraction and Timing Analysis	14:30 - 16:00	Room 4	Read More
3.5	Hardware authentication and attack prevention	14:30 - 16:00	Room 5	Read More
3.6	Software Solutions for Reliable Memories	14:30 - 16:00	Room 6	Read More
3.7	Design Automation of Cyber-Physical Systems	14:30 - 16:00	Room 7	Read More
IP1	Interactive Presentations	16:00 - 16:30	Poster Area	Read More
4.1	Executive Session 3: The Future of Test	17:00 - 18:30	Room 1	Read More
4.2	Reconfigurable Architecture and Tools	17:00 - 18:30	Room 2	Read More
4.3	Improving test generation and coverage	17:00 - 18:30	Room 3	Read More
4.4	Digital processing with emerging memory technologies	17:00 - 18:30	Room 4	Read More
4.5	Hardware Trojans and Split Manufacturing	17:00 - 18:30	Room 5	Read More
4.6	Smart Communication Solutions for Automotive Systems	17:00 - 18:30	Room 6	Read More
4.7	Energy and power efficiency in GPU-based systems	17:00 - 18:30	Room 7	Read More
4.8	Embedded Tutorial: Paving the Way for Very Large Scale Integration of Superconductive Electronics	17:00 - 18:30	Exh. Theatre	Read More

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