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[Home](#) > [Printer-friendly PDF](#) > [Printer-friendly PDF](#)

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Session Label	Session Title	Time	Location	Details
9.1	Special Day on "Model-Based Design of Intelligent Systems" Session: Experiences from the trenches, model-based design at work	08:30 - 10:00	Room 1	Read More
9.2	High-Level Synthesis	08:30 - 10:00	Room 2	Read More
9.3	Special Session: RISC-V or RISK-V? Towards Secure Open Hardware	08:30 - 10:00	Room 3	Read More
9.4	Where do NoC and Machine Learning meet?	08:30 - 10:00	Room 4	Read More
9.5	Attacking Memory and I/O Bottlenecks	08:30 - 10:00	Room 5	Read More
9.6	Reliability of highly-parallel architectures: an industrial perspective	08:30 - 10:00	Room 6	Read More
9.7	Runtime Predictability	08:30 - 10:00	Room 7	Read More
9.8	Special Session: IBM's Qiskit Tool Chain: Developing for and Working with Real Quantum Computers	08:30 - 10:00	Exh. Theatre	Read More
IP4	Interactive Presentations	10:00 - 10:30	Poster Area	Read More
10.1	Special Day on "Model-Based Design of Intelligent Systems" Session: Hot topic: Model-Based Machine Learning	11:00 - 12:30	Room 1	Read More
10.2	Special Session: Enabling Graph Analytics at Extreme Scales: Design Challenges, Advances, and Opportunities	11:00 - 12:30	Room 2	Read More
10.3	System-level Dependability for Multicore and Real-time Systems	11:00 - 12:30	Room 3	Read More
10.4	Disruptive Technologies Ain't Fake News!	11:00 - 12:30	Room 4	Read More
10.5	SSD and data placement	11:00 - 12:30	Room 5	Read More
10.6	Self-adaptive resource management	11:00 - 12:30	Room 6	Read More
10.7	Architectures for emerging machine learning techniques	11:00 - 12:30	Room 7	Read More
11.0	LUNCH TIME KEYNOTE SESSION	13:20 - 13:50	Room 1	Read More
11.1	Special Day on "Model-Based Design of Intelligent Systems" Session: MBD of Cyber-Physical Systems	14:00 - 15:30	Room 1	Read More
11.2	Novel techniques in optimization and high-level modeling of mixed-signal circuits	14:00 - 15:30	Room 2	Read More
11.3	Special Session: Rebooting our Computing Models	14:00 - 15:30	Room 3	Read More
11.4	Learning Gets Smarter	14:00 - 15:30	Room 4	Read More
11.5	Vitello e Mozzarella alla Fiorentina: Virtualization, Multicore, and Fault-Tolerance	14:00 - 15:30	Room 5	Read More
11.6	Design Automation Solutions for Microfluidic Platforms and Tasks	14:00 - 15:30	Room 6	Read More
11.7	Extending Scheduling Schemes	14:00 - 15:30	Room 7	Read More
IP5	Interactive Presentations	15:30 - 16:00	Poster Area	Read More
12.1	Special Day on "Model-Based Design of Intelligent Systems" Session: MBD of Safe and Secure Systems	16:00 - 17:30	Room 1	Read More
12.2	The Art of Synthesizing Logic	16:00 - 17:30	Room 2	Read More
12.3	Aging, calibration circuits and yield	16:00 - 17:30	Room 3	Read More
12.4	Design and Optimization for Low-Power Applications	16:00 - 17:30	Room 4	Read More
12.5	System Modelling for Analysis and Simulation	16:00 - 17:30	Room 5	Read More
12.6	Trojans and public key implementation challenges	16:00 - 17:30	Room 6	Read More
12.7	Emerging Strategies for Deep Neural Network Hardware	16:00 - 17:30	Room 7	Read More

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