



Published on DATE 2019 (<https://past.date-conference.com>)

[Home](#) > [Printer-friendly PDF](#) > Printer-friendly PDF

## 7.0 LUNCH TIME KEYNOTE SESSION

**Date:** Wednesday, March 27, 2019

**Time:** 13:50 - 14:20

**Location / Room:** Room 1

**Chair:**

Christoph Hagleitner, IBM Research, CH, [Contact Christoph Hagleitner](#)

**Co-Chair:**

Christian Plessl, Paderborn University, DE, [Contact Christian Plessl](#)

Time	Label	Presentation Title Authors
13:45	7.0.0	<b>CEDA LUNCHEON ANNOUNCEMENT</b> <b>Speaker:</b> David Atienza, EPFL, CH
13:50	7.0.1	<b>HETEROGENEOUS, HIGH SCALE COMPUTING IN THE ERA OF INTELLIGENT, CLOUD-CONNECTED DEVICES</b> <b>Author:</b> David Pellerin, Amazon, US <b>Abstract</b> <i>Rapid advances in connected devices, coupled with machine learning and "data lake" methods of advanced analytics, have led to an explosion in demand for non-traditional, highly scalable computing and storage platforms. This increasing demand is being seen in the public cloud as well as in cloud-connected IoT edge devices. AI/ML is at the heart of many the newest, most advanced analytics and IoT applications, ranging from robotics and autonomous vehicles, to cloud-connected products such as Alexa, to smart factories and consumer-facing services in the financial and healthcare sectors. In support of these important workloads, alternative methods of computing are being deployed in the cloud and at the edge. These alternative, heterogeneous computing methods include CPUs, GPUs, FPGAs, and other emerging acceleration technologies. This talk presents examples of such use-cases within Amazon, as well examples of how Amazon customers increasingly rely on AI/ML, accelerated using alternative computing methods and coupled with smart, cloud-connected devices to create next-generation intelligent products. The talk will conclude with examples of how cloud-based semiconductor design is being enhanced using these same methods.</i>
14:20		End of session
16:00		<b>Coffee Break</b> in Exhibition Area

### Coffee Breaks in the Exhibition Area

On all conference days (Tuesday to Thursday), coffee and tea will be served during the coffee breaks at the below-mentioned times in the exhibition area.

### Lunch Breaks (Lunch Area)

On all conference days (Tuesday to Thursday), a seated lunch (lunch buffet) will be offered in the Lunch Area to fully registered conference delegates only. There will be badge control at the entrance to the lunch break area.

Tuesday, March 26, 2019

- Coffee Break 10:30 - 11:30
- Lunch Break 13:00 - 14:30
- Keynote Lecture "[Leonardo da Vinci, Humanism and Engineering between Florence and Milan](#)" by [Claudio Giorgione](#) in room 1 13:50 - 14:20
- Coffee Break 16:00 - 17:00

Wednesday, March 27, 2019

- Coffee Break 10:00 - 11:00
- Lunch Break 12:30 - 14:30
- Keynote Lecture "[Heterogeneous, High Scale Computing in the Era of Intelligent, Cloud-Connected](#)" by [David Pellerin, Amazon, US](#) in room 1 13:50 - 14:20
- Coffee Break 16:00 - 17:00

Thursday, March 28, 2019

- Coffee Break 10:00 - 11:00
- University Booth Best Demo Award Presentation at the University Booth 10:30
- Lunch Break 12:30 - 14:00
- Keynote Lecture "[A Fundamental Look at Models and Intelligence](#)" by [Edward A. Lee, University of California, Berkeley, US](#) in room 1 13:20 - 13:50
- Coffee Break 15:30 - 16:00

**Source URL:** <https://past.date-conference.com/conference/session/7.0>