1.1 Opening Session

Date: Tuesday, March 25, 2014
Time: 08:30 - 10:30
Location / Room: Gessner Saal

Organiser:
Gerhard Fettweis, Technische Universität Dresden, DE, Contact Gerhard Fettweis

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08:30 1.1.1 WELCOME ADDRESSES
Speakers:
Gerhard Fettweis¹ and Luca Fanucci²
¹Technische Universität Dresden, DE; ²University of Pisa, IT

08:50 1.1.2 PRESENTATION OF DISTINGUISHED AWARDS
Speaker:
DATE Executive Committee , ,
Abstract
DATE 2014 Best Paper Awards
EDAA Lifetime Achievement Award 2014 (Rolf Ernst, TU Braunschweig, DE)
EDAA Outstanding Dissertation Awards 2013
ACM SIGDA Distinguished Service Award (Peter Marwedel, TU Dortmund, DE)
DATE Fellow Award (Enrico Macii, Politecnico di Torino, IT)
IEEE/CEDA Outstanding Service Contribution Award 2013 (Enrico Macii, Politecnico di Torino, IT)
IEEE CS TTTC Outstanding Contribution Award (Enrico Macii, Politecnico di Torino, IT)
IEEE Fellow Award (Tiziana Tera, University of Bologna, IT)

09:10 1.1.3 KEYNOTE ADDRESS: SYSTEM DESIGN CHALLENGES FOR NEXT GENERATION WIRELESS AND EMBEDDED SYSTEMS
Speaker:
David Fuller, National Instruments, US
Abstract
Application demands in our embedded world are growing dramatically. Consumer expectations and the industry’s forward-looking technology roadmaps paint a picture of a connected world full of intelligent devices once thought to have fixed functionalities. Researchers exploring next generation wireless systems, Internet of Things (IoT), and even machine-to-machine (M2M) communications face many challenges in making this vision a reality. Where once a single, isolated design flow addressed the discrete application, heterogeneous multi-processing architectures must be considered and embraced along with the connections to other devices and systems, and real-world sensor data. As the systems grow in complexity, new design approaches must also be developed and employed to expedite the research, design, and development cycle. David Fuller will outline challenges system designers face in developing cyber-physical systems and explore a graphical system design approach that includes hardware abstraction and comprehends a heterogeneous multiprocessing environment while embracing different models of computation. Through this new approach, system designers can shorten design cycles and the time to prototype ultimately accelerating deployment.

10:30 End of session
Coffee Break in Exhibition Area
On Tuesday-Thursday the coffee and lunch breaks will be located in the Exhibition Area (Terrace Level).

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