Call for Papers: Conference and Exhibition; March 14-18, 2016, ICC, Dresden, Germany

2015-04-16

Areas of Interest

Within the scope of the conference, the main areas of interest are: embedded systems, design methodologies, CAD languages, algorithms and tools, testing of electronic circuits and systems, embedded software, applications design and industrial design experiences. Topics of interest include, but are not restricted to:

- System Specification and Modeling
- System Design, Synthesis and Optimization
- Simulation and Validation
- Design of Low Power Systems
- Temperature-Aware Design
- Power Estimation and Optimization
- Temperature Modeling and Management
- Emerging Technologies, Systems and Applications
- Formal Methods and Verification
- Network on Chip
- Architectural and Microarchitectural Design
- Architectural and High-Level Synthesis
- Reconfigurable Computing
- Logic and Technology Dependent Synthesis for Deep-Submicron Circuits
- Physical Design and Verification
- Analogue and Mixed-Signal Circuits and Systems
- Interconnect, EMC, EMD and Packaging Modeling
- Multiprocessor System-on-Chip and Computing Systems
- Communication, Consumer and Multimedia Systems
- Transportation Systems
- Medical, Healthcare and Assistive Technology Systems
- Energy Generation, Recovery and Management Systems
- Secure, Dependable and Adaptive Systems
- Test for Defects, Variability, and Reliability
- Test Generation, Simulation and Diagnosis
- Test for Mixed-Signal, Analog, RF, MEMS
- Test Access, Design-for-Test, Test Compression, System Test
- On-Line Testing and Fault Tolerance
- Real-time, Networked and Dependable Systems
- Compilers and Code Generation for Embedded Systems; Software-centric System Design Exploration
- Model-based Design and Verification for Embedded Systems
- Embedded Software Architectures and Principles; Software for MPSoC, Multi/many-core and GPU-based Systems

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The DATE 2015 conference and exhibition in Grenoble attracted 1450 experts

2015-03-18

The DATE 2015 Conference and Exhibition closed doors last Friday, March 13, 2015, receiving 1450 registrations from all over the world and ending with a positive feedback from both participants and exhibitors.

DATE 2015 received eligible 915 paper submissions – an all-time high for DATE and a 3% increase over DATE 2014. Besides the large share (45%) of submissions coming from Europe, 24% of submissions are from North-America, 28% from Asia, and 3% from the rest of the world. This manifests DATE’s international character, its global reach and world-wide impact.

For the 18th successive year, DATE presented an exciting technical programme, comprising 78 technical sessions and 11 Exhibition Theatre sessions. The DATE week started on Monday with ten in-depth tutorials and the ACM SIGDA/EDAA PhD Forum.

On Tuesday, the conference was opened by the plenary keynote speakers Günther H. Oettinger, EU Commissioner for Digital Economy and Society, who talked about European Microelectronics Strategy, and Laurent Rémont, STMicroelectronics Group Vice President for Technology and Product strategy, Embedded Processing Solutions Segment, who presented a talk on “Embracing the Digital Transformation & Enabling IoT Innovations”. On the same day, Mentor Graphics organized a successful lunchtime keynote session on “How micro-electronic will change your lifestyle” and the Executive Track offered a series of business panels discussing hot topics. The accompanying exhibition comprised 20 exhibiting companies, including 5 European Projects.

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Impressions from DATE 2015

2015-03-11

Tutorials (Read more ...) and EDAA/ACM SIGDA PhD Forum (Read more ...)
Opening Session

Read more ...
Awards Ceremony

Read more ...
DATE’15 Best Paper Awards – A Track
HARDWARE TRAP DETECTION FOR GATE-LEVEL ICS USING SIGNAL CORRELATION BASED CLUSTERING
Burcin Caktir and Akif Aliak

DATE’15 Best Paper Awards – T Track
DIGITAL CIRCUITS RELIABILITY WITH IN-SITU MONITORS IN 28NM FULLY DEPLETED SOI
Marine Saliva, Floria Abalo, Vincent Baudard, Xavier Federspiel, Damien Brugot, Abdelmadho
Awardees

- New directions in embedded system design and embedded software
  - Nan Guan, Ph.D. (Uppsala University, Sweden; Advisor: Prof. Wang Yi)
  - "New Techniques for Building Timing-Predictable Embedded Systems"

- New directions in logic and system design
  - Zhenyu Sun, Ph.D. (University of Pittsburgh, USA; Advisor: Prof. Hai Li)
  - "High-Performance And Low-Power Memory Based Cache Design"
Awardees

- New directions in physical design, design for manufacturing and CAD for analogue circuits and MEMS
  - Bei Yu, Ph.D. (University of Texas at Austin, USA); Adviser: Prof. David Z. Pan
  - "Design for Manufacturing with Analog and Mixed-Signal Applications"

- New directions in circuit and systems design
  - Brandon Robert Noia, Ph.D. (Delft University of Technology, The Netherlands); Adviser: Prof. Knshnendu Chakrabarty
  - "Design-for-Test and Test Optimization for Mixed-Signal and 3D Stacked ICs"
DATE Fellow Award
Gerhard Fettweis
Technical University of Dresden
For outstanding contributions as General Chair of DATE 2015.
KEYNOTE ADDRESS: EUROPEAN MICROELECTRONICS STRATEGY

This morning EU Commissioner Günther H. Oettinger gave a keynote at DATE 2015. You can find his speech at https://ec.europa.eu/commission/2014-2019/oettinger/announcements/speech...

University Booth at DATE 2015 - Final Programme

The University Booth is organized during DATE and will be located in the exhibition area at booth 4. All demonstrations will take place from Tuesday, March 10 to Thursday, March 12, 2015 during DATE. Universities and public research institutes have been invited to submit hardware or software demonstrations.

The University Booth program is composed by 39 demonstrations from 14 different countries, presenting software and hardware solutions. The program is organized in 11 sessions of 2 or 2.5 h duration and will cover 4 major topics:

- Electronic Design Automation Prototypes
- Hardware Design and Test Prototypes
EDAA Lifetime Achievement Award 2015 goes to Lothar Thiele
2015-02-28
Leuven, Belgium, February 28, 2015 – The EDAA Lifetime Achievement Award 2015 goes to Lothar Thiele.

The EDAA Lifetime Achievement Award is given to individuals who have made outstanding contributions to the state of the art in electronic design, automation and testing of electronic systems during their career. In order to be eligible, candidates must have made innovative contributions which had an impact on the way electronic systems are being designed.


The Award will be presented at the plenary session of the 2015 DATE Conference, to be held March 9-12 in Grenoble, France (https://www.date-conference.com).

DATE 2015 Event Highlights Designing Electronics for the Internet of Things (IoT) and for Medical Applications
2015-02-09
DATE 2015 opens doors on March 09 at the Alpexpo Congress Centre, Grenoble, France

On Tuesday, the conference will be opened by the plenary keynote speakers Geneviève Fioraso, secrétaire d'Etat chargée de l'Enseignement supérieur et de la Recherche (to be confirmed), EU Commissioner for Digital Economy and Society Günther H. Oettinger to talk about European Microelectronics Strategy and Jean Marc Chery, Chief Operating Officer of STMicroelectronics, who will talk about “ST technologies fully addressing Internet of Things applications from LP Digital to RF-CMOS, eNVM and sensors”.

On the same day, a keynote session sponsored by Mentor Graphics is organized during lunchtime. Moreover, the Executive Track offers a series of business panels discussing hot topics. Executive speakers from companies leading the design and automation industry will address some of the complexity issues in electronics design and discuss about advanced technology challenges and opportunities.

The distinctive highlights of DATE 2015 are the two special days: “Designing Electronics for the Internet of Things (IoT)” on Wednesday, and “Designing Electronics for Medical Applications” on Thursday, each giving a comprehensive overview regarding both themes, ranging from innovations and applications of latest technologies to the analysis of upcoming electronic design automation (EDA) research challenges.

The two keynote speakers on Wednesday comprise Wolfgang Wahlster (German Research Center for Artificial Intelligence, DE) to talk about “Industrie 4.0: From the Internet of Things to Cyber-Physical Production Systems” and Antun Domic (Synopsys, US) who will analyze “The Rise of IoT, and the Role of EDA”. On Thursday, the keynote speaker Kristoffer Famm (GlaxoSmithKline, GB) is going to talk about “Bioelectronic Medicines - Heralding in a New Therapeutic Approach”.

The full conference programme including 10 in-depth tutorials on March 09, the regular conference from March 10 to 12, and ten parallel Friday Workshops on March 13, 2015, is available at: https://www.date-conference.com/conference/event-overview

DATE 2015: Advance Programme available
2015-01-13
DATE 2015: Advance Programme available online at https://www.date-conference.com/conference/event-overview

We proudly present to you the Advance Programme of DATE 2015. DATE combines the world’s favorite electronic systems design and test conference with an international exhibition for electronic design, automation and test, ranging from advances on system-level hardware and software implementation to integrated circuit design and nano-technology manufacturing technologies.

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For the 18th successive year, DATE has prepared an exciting technical programme. With the help of the 309 members from its Technical Programme Committee who carried out 3675 reviews (more than four per submission), finally 206 papers (22%) were selected for regular presentation and 86 additional ones (9%) for interactive presentation this year.

Conference Highlights@Computingnow: Design Automation Test Europe 2015
2014-12-23
DATE 2015: Advance Programme available

DATE 2015: Advance Programme available online at https://www.date-conference.com/conference/event-overview

We proudly present to you the Advance Programme of DATE 2015. DATE combines the world’s favorite electronic systems design and test conference with an international exhibition for electronic design, automation and test, from system-level hardware and software implementation right down to integrated circuit design.

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Source URL: https://past.date-conference.com/date15/front