DATE 2017 in Lausanne received over 1,500 registrations from international experts, who celebrated the 20th edition of the conference and exhibition

2017-04-04

The DATE 2017 Conference and Exhibition closed doors last Friday, March 31, 2017, receiving more than 1,500 registrations from over 50 countries and ending with excellent feedback from both participants and exhibitors.

2017 was a special year for the world's favourite electronic systems design and test conference, as the community celebrated its 20th edition and it was held for the first time in Switzerland, at the SwissTech Convention Center on the EPFL campus in Lausanne, from March 27 to 31, 2017.

DATE 2017 received 794 paper submissions, a large share (42%) coming from authors in Europe, 32% from America, 25% from Asia, and 1% from the rest of the world. This clearly demonstrates DATE's international character, its global reach and impact.

For the 20th successive year, DATE presented an exciting technical programme, comprising 78 technical sessions and 11 Exhibition Theatre sessions. The DATE week started on Monday with nine in-depth tutorials and the popular PhD Forum, hosted by EDAA, ACM SIGDA, and IEEE CEDA.

On Tuesday, the conference was opened by the plenary keynote speakers Dr. Anvind Krishna from IBM presenting "Design Automation in the Era of AI and IoT: Challenges and Pitfalls" and Dr. Doug Burger from Microsoft who talked about "A New Era of Hardware Microservices in the Cloud". In addition, a keynote lecture from Prof. Giovanni De Micheli (Professor at EPFL and Director of Nano-Tera.ch) was given on "Distributed Systems for Health and Environmental Monitoring" within the Nano-Tera.ch initiative. On the same day, an Executive Panel Session and three Innovative Technologies & Applications (IT&A) Sessions offered a series of business panels discussing hot topics. Executive speakers from companies leading the design and automation industry will address some of the complexity issues in electronics design and discuss about the advanced technology challenges and opportunities. A special session was dedicated to Ralph Otten, pioneer of physical design, who prematurely died in an accident.

DATE 2017, Lausanne: Thank you! +++ Save the DATE 2018: 19 – 23 March, 2018 in Dresden, Germany

2017-03-30

DATE 2018 will take place from March 19 – 23, 2018, in Dresden, Germany, and will be chaired by Jan Madsen, DTU, Lyngby, Denmark.

The detailed Exhibition & Sponsorship Brochure and further information will be sent in June, 2017.

In case of any questions in the meantime, please do not hesitate to contact us.

Conference Organisation

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News from our App

2017-03-30

RT @ECSEL_C4D: UNIVAQ and UNISS presented demo and poster: "JOINTER: Joining flexible monitors with heterogeneous architectures" at Univers…
May 25, 2020 5:51 PM

RT @ECSEL_C4D: Tiziana Fanni, UNISS presented demo and poster: "JOINTER: Joining flexible monitors with heterogeneous architectures" at Univers…
May 16, 2020 11:57 AM

UNIVAQ and UNISS presented demo and poster: "JOINTER: Joining flexible monitors with heterogeneous architectures" at Univers…
May 14, 2020 4:05 PM
Here are the winner photos!
Fortunately, we did not need the rescue rings for the sessions!
Awards Ceremony

Read more ...
Kai Hu, Ph.D. (Duke University, USA; Advisor: Prof. Chakrabarty);

"Optimization, Testing and Design-for-Flow-Based Microfluidic Biochips"

Veit Benedikt Klees (TUM, DE; Advisor: Prof. ...);

"Resilient Cross-Layer Design of Digital Circuits"
DATE 2017 Awards Ceremony
2017-03-28

PhD Forum Best Poster Prize supported by EDAA, ACM Sigda and IEEE CEDA
Enabling Caches in Probabilistic Timing Analysis
Leonidas Kosmidis, Barcelona Supercomputing Center, ES

Towards Computer-Aided Design of Quantum Logic
Philipp Niemann, University of Bremen, DE

IEEE Fellow Award

Prof. Todd Austin
University of Michigan
elevated to IEEE Fellow “for contributions to simulation techniques and resilient system design in computer architecture”

Prof. Valeria Bertacco
University of Michigan
elevated to IEEE Fellow "for contribution to computer-aided verification and reliable system design"

Prof. Cristina Silvano
Politecnico di Milano
elevated to IEEE Fellow “for contributions to energy-efficient computer architectures”

Prof. Alex Yakovlev
Newcastle University
elevated to IEEE Fellow “for contributions to theory and design of asynchronous circuits”

DATE Fellow Award
Dr. Juergen Haase
edacentrum
For long term outstanding contribution to the DATE Exhibition Theatre

Prof. Luca Fanucci
Università di Pisa
For outstanding service contribution as General Chair of DATE 2016

IEEE CEDA Service Award
Prof. Luca Fanucci
For outstanding service contribution as General Chair of DATE 2016

IEEE CS TTTC Outstanding Contribution Award
IEEE TTTC Lifetime Contribution Medal

Bernard Courtois
For outstanding technical contribution and fundamental impact on Test Technology

DATE 2017 Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the best papers. The selection is performed by an award committee, based on the results of the reviewing process and the quality of the final paper.

DATE Best Paper Awards have been given during the DATE Party

D Track
AUTOMATIC PLACE-AND-ROUTE OF EMERGING LED-DRIVEN WIRES WITHIN A MONOLITHICALLY-INTEGRATED CMOS+III-V PROCESS
Tushar Krishna¹, Arya Balachandran², Siau Ben Chiah², Li Zhang², Bing Wang³, Cong Wang³, Kenneth Lee Eng Kian³, Jurgen Michel⁴ and Li-Shiuan Peh⁵
¹Georgia Institute of Technology, US; ²NTU, SG; ³SMART, SG; ⁴MIT, US; ⁵Professor, National University of Singapore, SG

Visit Session ...

A Track
COSYN: EFFICIENT SINGLE-CELL ANALYSIS USING A HYBRID MICROFLUIDIC PLATFORM
Mohamed Ibrahim¹, Krishnendu Chakrabarty¹ and Ulf Schlichtmann²
¹Duke University, US; ²TU München, DE

Visit Session ...

T Track
FAST AND WAVEFORM-ACCURATE HAZARD-AWARE SAT-BASED TSOF ATPG
Jan Burchard³, Dominik Erb¹, Adit D. Singh¹, Sudhakar M. Reddy¹ and Bernd Becker¹
³University of Freiburg, DE; ¹Auburn University, US; ²University of Iowa, US

Visit Session ...

E Track
MODNN: LOCAL DISTRIBUTED MOBILE COMPUTING SYSTEM FOR DEEP NEURAL NETWORK
Jiachen Mao¹, Xiang Chen¹, Kent W. Nixon¹, Christopher Krieger² and Yiran Chen¹
¹University of Pittsburgh, US; ²George Mason University, US; ³University of Maryland, Baltimore County, US

Visit Session ...

DATE 2017 Best IP Award

DATE Best IP Award has been given during the DATE Party

EVOAPPROXIB: LIBRARY OF APPROXIMATE ADDERS AND MULTIPLIERS FOR CIRCUIT DESIGN AND BENCHMARKING OF APPROXIMATION METHODS  (Paper/SoftConf ID: 241)
Vojtech Mrazek, Raden Hrstacek, Zdenek Vacek and Lukas Sekanina, Brno University of Technology, CZ

Visit Session ...
EDAA Achievement Award 2017 goes to Prof. C. L. David Liu
2017-03-28

The EDAA Achievement Award is given to individuals who have made outstanding contributions to the state of the art in electronic design, automation and testing of electronic systems during their career. In order to be eligible, candidates must have made innovative contributions, which had an impact on the way electronic systems are being designed.

Prof. C. L. David Liu received his B. Sc. degree at the National Cheng Kung University in Taiwan and his S. M. and Sc. D. degrees from the Massachusetts Institute of Technology. His teaching career spans over forty years, at MIT, the University of Illinois at Urbana Champaign, and the National Tsing Hua University, where he is now the William Mong Honorary Chair Professor of Computer Science. His academic administrative duties include serving as Associate Provost at the University of Illinois at Urbana Champaign from 1996 to 1998, and as President of the National Tsing Hua University from 1998 to 2002.

Outstanding Dissertation Award - The Winners are ...
2017-03-28

New directions in software design and optimization for embedded, cyber-physical and secure systems
Marco Zimmerling, Ph.D. (ETHZ, CH; Advisor: Prof. Lothar Thiele)
"End-to-end Predictability and Efficiency in Low-power Wireless Networks"

New directions in system-on-chip platforms co-design, novel architectures and system-level management
Bingjun Xiao, Ph.D. (UCLA, US; Advisor: Prof. Jason Cong)
"Communication Optimization for Customizable Domain-Specific Computing"

New directions in logic, physical design and CAD for analog/mixed-signal, nano-scale and emerging technologies
Kai Hu, Ph.D. (Duke University, USA; Advisor: Prof. Krishnendu Chakrabarty)
"Optimization, Testing and Design-for-Testability of Flow-Based Microfluidic Biochips"

New directions in robust circuit design, reliability-aware design and system test
Veit Benedikt Kleeberger, Ph.D. (TUM, DE; Advisor: Prof. Ulf Schlichtmann)
"Resilient Cross-Layer Design of Digital Integrated Circuits"

General Information Registration & Participation
2017-03-15

The online registration for DATE 2017 closed on Wednesday, 15 March 2017 12:00:00 CET.

The registration to the conference will still be possible on-site at the registration desk (SwissTech Convention Center).

In case of any questions, please do not hesitate to contact the DATE Conference Organization via email: date@kitdresden.de.

DATE 2017 Event Highlights Electronics for the Internet of Things Era and Wearable and Smart Medical Devices
2017-02-28

DATE 2017 opens doors on March 27 at the SwissTech Convention Center in Lausanne, Switzerland

2017 is a special year for the world’s favourite electronic systems design and test conference, as we celebrate its 20th edition and it will be held for the first time in Switzerland, at the SwissTech Convention Center on the EPFL campus in Lausanne, from March 27 to 31, 2017.

On Tuesday, the conference will be opened by the plenary keynote speakers Dr. Anvind Krishna from IBM who will talk about “Design Automation in the Era of AI and IoT: Challenges and Pitfalls” and Dr. Doug Burger from Microsoft to talk about “A New Era of Hardware Microservices in the Cloud”. In addition, a keynote lecture from Prof. Giovanni De Micheli (Professor at EPFL and Director of Nano-Tera.ch) will be given on Tuesday on “Distributed Systems for Health and Environmental Monitoring” within the Nano-Tera.ch initiative. On the same day, the Executive Track, including one Executive Panel Session and three Innovative Technologies & Applications (IT&A) Sessions, offers a series of business panels discussing hot topics. Executive speakers from companies leading the design and automation industry will address some of the complexity issues in electronics design and discuss about the advanced technology challenges and opportunities. A special session is dedicated to Ralph Otten, pioneer of physical design, who prematurely died in an accident.

Two Special Days in the programme will focus on areas bringing new challenges to the system design community: Designing Electronics for the Internet of Things Era and Designing Wearable and Smart Medical Devices, each having a full programme of keynotes, panels, tutorials and technical presentations.
The final programme of the University Booth at DATE 2017 is available online here and for download as PDF document within the DATE booklet available here.

The University Booth is organised during DATE and will be located in booth 1 of the exhibition area. All demonstrations will take place from Tuesday, March 28 to Thursday, March 30, 2017 during DATE. Universities and public research institutes have been invited to submit hardware or software demonstrators.

The University Booth programme is composed of 39 demonstrations from 13 different countries, presenting software and hardware solutions. The programme is organised in 11 sessions of 2 or 2.5 h duration and will cover the topics:

- Electronic Design Automation Prototypes
- Hardware Design and Test Prototypes
- Internet-of-Things Prototypes
- Wearable Electronics and Smart Medical Prototypes

The University Booth at DATE 2017 invites you to booth 1 to find out more about the latest trends in software and hardware from the international research community.

Source URL: https://past.date-conference.com/date17/front