

Electro-, Stress- and Thermomigration: Three Forces, One Problem

Steve Bigalke

Institute of Electromechanical and Electronic Design
Dresden University of Technology, Germany
✉ steve.bigalke@outlook.com

Jens Lienig

Institute of Electromechanical and Electronic Design
Dresden University of Technology, Germany
✉ jens@ieee.org

Abstract—It is well-known that the downscaling of micro-electronic structures (“Moore’s Law”) reduces the reliability due to an increase in potential material migration. Electro-, stress- and thermomigration have been identified as the main causes of material dislocation in integrated circuits (ICs). They are driven by current densities, stress and temperature gradients, respectively, but they also depend on common parameters like material constants. While each of these three driving forces causes migration, they can compensate or amplify each other, resulting in various overall material dislocations. These interactions are poorly understood which complicates the prevention of migration processes in ICs. Our software demonstrator presents a basic approach to identify the predominate migration within various circuit conditions including the interaction of all three forces. Our approach can also be adjusted to three-dimensional circuits (3D ICs) and alternating conditions.

I. INTRODUCTION

The impact of material transport on interconnect structures rises with their downscaling. Electromigration (EM), stress migration (SM) and thermomigration (TM) have been identified as the main causes of material dislocation within those structures. To fully understand the migration process of interconnects, one must investigate these three phenomena for a given circuit condition. This is crucial, because they can compensate or amplify each other, differ in their orders of magnitude and depend on mutual factors of influence.

II. MATERIAL MIGRATION

A. Electromigration

The atomic flux due to EM (J_{EM}) depends on the concentration of atoms (C), diffusion coefficient or diffusivity (D), Boltzmann’s constant (k), average temperature ($\bar{T}$), charge of an electron (e), effective charge number (Z^*), resistivity (ρ) and current density (j) [1]:

$$\vec{J}_{EM} = CD/k\bar{T} \cdot eZ^* \rho \vec{j}. \quad (1)$$

This equation clearly shows that the driving force of EM is current density, however, it also depends on other factors such as temperature.

B. Stress migration

In addition to the parameters from Eq. (1), the atomic flux of SM (J_{SM}) depends on atomic volume (Ω) and hydrostatic stress (σ_H) [1]:

$$\vec{J}_{SM} = CD/k\bar{T} \cdot \Omega \vec{\nabla} \sigma_H. \quad (2)$$

The main driving force of J_{SM} is the stress gradient. This gradient can be caused by unbalanced layer growth, external bending, different coefficients of thermal expansion (CTE) and accumulation or depletion of atoms in an encapsulated interconnect.

C. Thermomigration

Ancillary to the parameters from Eq. (1) and (2), the atomic flux of TM (J_{TM}) depends on heat of transport (Q^*) and local temperature (T) [1]:

$$\vec{J}_{TM} = CD/k\bar{T}^2 \cdot Q^* \vec{\nabla} \bar{T}. \quad (3)$$

It can be seen that a local temperature gradient within an interconnect is the main driving force. An active self-heating process or a passive heat source can be reason for this.

D. Interaction

The total atomic flux (J_{total}) in interconnects is defined as [2]:

$$\vec{J}_{total} = \vec{J}_{EM} + \vec{J}_{SM} + \vec{J}_{TM}, \quad (4)$$

whereby only a negative or positive divergence of the total atomic flux (∇J_{total}) indicates void or hillock creation, respectively. Figure 1 shows one possible interaction of EM, SM and TM and their individual driving forces [3].

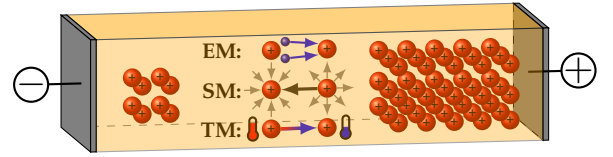


Fig. 1. Atom migration within an interconnect due to EM, SM and TM and their individual driving forces.

III. SOFTWARE DEMONSTRATOR

Our software demonstrator is tailored to determine the impact of EM, SM and TM for a given range of circuit conditions. We can identify the predominate migration with respect to the interaction with other migrations. It is also possible to investigate the impact change of EM, SM and TM, individually, due to increasing or decreasing driving forces.

IV. CONCLUSION

The mutual compensation or amplification of EM, SM and TM must be considered to fully understand (and prevent) the migration process of interconnects. Negligence of one of them is only reasonable if it is orders of magnitude smaller than the others.

REFERENCES

- [1] T. King-Ning, *Electronic Thin-Film Reliability*. Cambridge University Press, Cambridge, 2010, DOI: 10.1017/CBO9780511777691.
- [2] C. M. Tan and F. He, *Electromigration Modeling at Circuit Layout Level*. Springer, Singapore, 2013, DOI: 10.1007/978-981-4451-21-5.
- [3] K. Weide-Zaage, D. Dalleau, and X. Yu, “Static and dynamic analysis of failure locations and void formation in interconnects due to various migration mechanisms,” *Materials Science in Semiconductor Processing*, vol. 6, no. 13, pp. 85 – 92, 2003, DOI: 10.1016/S1369-8001(03)00075-1.