

## Technical Programme Topic Chairs 2018

<b>Track D</b>	Design Methods and Tools
<b>Track Chair</b>	Ayse Coskun, <i>Boston University, US</i>

<b>D1 System Specification and Modeling</b>	<b>D2 System Design, High-Level Synthesis, and Optimization</b>	<b>D3 System Simulation and Validation</b>
<b>Chair:</b> Ingo Sander, <i>KTH Royal Institute of Technology, SE</i>	<b>Chair:</b> Yuko Hara-Azumi, <i>Tokyo Institute of Technology, JP</i>	<b>Chair:</b> Valeria Bertacco, <i>University of Michigan, US</i>
<b>Co-Chair:</b> Andreas Gerstlauer, <i>University of Texas at Austin, US</i>	<b>Co-Chair:</b> Philippe Coussy, <i>Universite de Bretagne-Sud / Lab-STICC, FR</i>	<b>Co-Chair:</b> Graziano Pravadelli, <i>University of Verona, IT</i>
<b>D4 Formal Methods and System Verification</b>	<b>DT5 Design and Test for Analog and Mixed-Signal Circuits and Systems, and MEMS</b>	<b>DT6 Design and Test of Secure Systems</b>
<b>Chair:</b> Christoph Scholl, <i>University of Freiburg, DE</i>	<b>Chair:</b> André Ivanov, <i>University of British Columbia, CA</i>	<b>Chair:</b> Ilia Polian, <i>University of Stuttgart, DE</i>
<b>Co-Chair:</b> Armin Biere, <i>Johannes-Kepler-University Linz, AT</i>	<b>Co-Chair:</b> Georges Gielen, <i>Katholieke Universiteit Leuven, BE</i>	<b>Co-Chair:</b> Lejla Batina, <i>Radboud University Nijmegen, NL</i>
<b>D7 Network on Chip</b>	<b>D8 Architectural and Microarchitectural Design</b>	<b>D9 Power Modeling, Low-Power Design, and Power-Aware Optimization</b>
<b>Chair:</b> Luca Carloni, <i>Columbia, US</i>	<b>Chair:</b> Eli Bozorgzadeh, <i>Univ. of California, Irvine, US</i>	<b>Chair:</b> Alberto Macii, <i>Politecnico di Torino, IT</i>
<b>Co-Chair:</b> Sebastien Le Beux, <i>Lyon Institute of Nanotechnology, FR</i>	<b>Co-Chair:</b> Francisco Cazorla, <i>Barcelona Supercomputing Center and IIIA-CSIC, ES</i>	<b>Co-Chair:</b> Naehyuck Chang, <i>KAIST, KR</i>

<b>D10 Temperature and Variability-Aware System Design and Optimization</b>	<b>D11 Reconfigurable Computing</b>	<b>D12 Logical and Physical Analysis and Design</b>
<b>Chair:</b> Giovanni Ansaloni, <i>USI - Lugano, CH</i>	<b>Chair:</b> Fabrizio Ferrandi, <i>Politecnico di Milano, IT</i>	<b>Chair:</b> Tiziano Villa, <i>Dipartimento d'Informatica, Universita' di Verona, IT</i>
<b>Co-Chair:</b> Jose L. Ayala, <i>Complutense University of Madrid, ES</i>	<b>Co-Chair:</b> Florent de Dinechin, <i>INSA-Lyon, FR</i>	<b>Co-Chair:</b> Elena Dubrova, <i>Royal Institute of Technology - KTH, SE</i>
<b>D13 Emerging Technologies for Future Computing</b>	<b>D14 Emerging Technologies for Future Memories</b>	
<b>Chair:</b> Aida Todri-Sanial, <i>CNRS-LIRMM/University of Montpellier, FR</i>	<b>Chair:</b> Jean-Michel Portal, <i>Aix-Marseille University, FR</i>	
<b>Co-Chair:</b> Walter Weber, <i>NaMLab gGmbH and CfAED, DE</i>	<b>Co-Chair:</b> Pierre-Emmanuel Gaillardon, <i>University of Utah, US</i>	

<b>Track A</b>	Application Design
<b>Track Chair</b>	Ian O'Connor, Lyon Institute of Nanotechnology, FR

<b>A1 Power-efficient and Sustainable Computing</b>	<b>A2 Communication, Consumer and Multimedia Systems</b>	<b>A3 Automotive Systems and Smart Energy Systems</b>
<b>Chair:</b> Muhammad Shafique, <i>Vienna University of Technology (TU Wien), AT</i>	<b>Chair:</b> Steffen Paul, <i>University Bremen, DE</i>	<b>Chair:</b> Davide Brunelli, <i>University of Trento, IT</i>
<b>Co-Chair:</b> Baris Aksanli, <i>San Diego State University, US</i>	<b>Co-Chair:</b> Theocharis Theocharides, <i>University of Cyprus, CY</i>	<b>Co-Chair:</b> Sebastian Steinhorst, <i>Technical University of Munich, DE</i>
<b>A4 Healthcare, Wellness, and Assistive Technologies</b>	<b>A5 Secure Systems, Circuits, and Architectures</b>	<b>A6 Reconfigurable and Robust Systems</b>
<b>Chair:</b> Elisabetta Farella, <i>Fondazione Bruno Kessler (FBK), IT</i>	<b>Chair:</b> Tim Güneysu, <i>University of Bremen &amp; DFKI, DE</i>	<b>Chair:</b> Christian Weis, <i>University of Kaiserslautern, DE</i>
<b>Co-Chair:</b> Joaquín Recas, <i>Computer Science Faculty, Complutense University of Madrid, ES</i>	<b>Co-Chair:</b> Stefan Mangard, <i>Graz University of Technology, AT</i>	<b>Co-Chair:</b> Antonio Miele, <i>Politecnico di Milano, IT</i>

<b>A7 Applications of Emerging Technologies</b>	<b>A8 Industrial Experiences Brief Papers</b>
---	---

<b>Chair:</b> Andy Tyrrell, <i>University of York, GB</i>	<b>Chair:</b> Fabien Clermidy, <i>CEA-Leti, FR</i>
<b>Co-Chair:</b> Yu Wang, <i>Tsinghua University Beijing, CN</i>	<b>Co-Chair:</b> Norbert Wehn, <i>University of Kaiserslautern, DE</i>

<b>Track T</b>	Test and Robustness
<b>Track Chair</b>	Cristiana Bolchini, Politecnico di Milano, IT

<b>T1 Modeling and Mitigation of Defects, Faults, Variability, and Reliability</b>	<b>T2 Test Generation, Simulation and Diagnosis</b>	<b>T3 Design-for-Test, Test Infrastructures, Test Standards</b>
--	---	---

<b>Chair:</b> Mehdi Tahoori, <i>Karlsruhe Institute of Technology, DE</i>	<b>Chair:</b> Matteo Sonza Reorda, <i>Politecnico di Torino - DAUIN, IT</i>	<b>Chair:</b> Sybille Hellebrand, <i>University of Paderborn, DE</i>
<b>Co-Chair:</b> Said Hamdioui, <i>Delft University of Technology, NL</i>	<b>Co-Chair:</b> Davide Appello, <i>STMicroelectronics, IT</i>	<b>Co-Chair:</b> Jerzy Tyszer, <i>Poznan University of Technology, PL</i>

<b>T4 System-Level Reliability Design, Analysis and On-line Test</b>	<b>DT5 Design and Test for Analog and Mixed-Signal Circuits and Systems, and MEMS</b>	<b>DT6 Design and Test of Secure Systems</b>
--	---	--

<b>Chair:</b> Jaume Abella, <i>Barcelona Supercomputing Center (BSC-CNS), ES</i>	<b>Chair:</b> André Ivanov, <i>University of British Columbia, CA</i>	<b>Chair:</b> Ilia Polian, <i>University of Stuttgart, DE</i>
<b>Co-Chair:</b> Maria K. Michael, <i>University of Cyprus, CY</i>	<b>Co-Chair:</b> Georges Gielen, <i>Katholieke Universiteit Leuven, BE</i>	<b>Co-Chair:</b> Lejla Batina, <i>Radboud University Nijmegen, NL</i>

<b>Track E</b>	Embedded and Cyber-physical Systems
<b>Track Chair</b>	Franco Fummi, Universita' di Verona, IT

<b>E1 Real-time, Networked, and Dependable Systems</b>	<b>E2 Compilers and Software Synthesis</b>	<b>E3 Model-based Design and Verification</b>
--	--	---

<b>Chair:</b> Kai Lampka, <i>Electrobit Automotive GmbH, DE</i>	<b>Chair:</b> Frank Hannig, <i>Friedrich-Alexander-Universität Erlangen-Nürnberg, DE</i>	<b>Chair:</b> Petru Eles, <i>Linköping University, SE</i>
<b>Co-Chair:</b> Dionisio de Niz, <i>Carnegie Mellon University, US</i>	<b>Co-Chair:</b> Bjorn De Sutter, <i>Ghent University, BE</i>	<b>Co-Chair:</b> Borzoo Bonakdarpour, <i>University of Waterloo, CA</i>

#### **E4 Embedded Software Architectures**

**Chair:** Akash Kumar, *Technische  
Universitaet Dresden, DE*

**Co-Chair:** Orlando Moreira, *Intel  
Corporation, PT*

#### **E5 Software architectures for Cyber- Physical Systems**

**Chair:** Shiyang Hu, *Michigan Technological  
University, US*

**Co-Chair:** Thomas Nolte, *Control, SE*