

Embedded Software Debug and Test

Needs and Requirements for Innovations in Debugging

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Abstract— Today's complexity of embedded software is steadily increasing. The growing number of processors in a system and the increased communication and synchronization of all components requires scalable debug and test methods for each component as well as the system as a whole. Considering today's cost and time to market sensitivity it is important to find and debug errors as early as possible and to increase the degree of test and debug automation to avoid the loss of quality, cost and time. These challenges are not only requiring new tools and methodologies but also organizational changes since hardware and software developer have to work together to achieve the necessary productivity and quality gain.

This panel brings together users and solution provider experienced in debugging embedded systems to discuss requirements for robust systems that are easy to debug.

embedded systems, software debugging,model-based software debug, test automation, virtual prototype, hardware-software co-verification, silicon debug,debug standards

I. PANELISTS

- Francois Cerisier, Senior IC Verification Consultant and EDA Activities Manager, EASii-IC, France
- Simon Davidmann, CEO, Imperas, UK
- Laurent Ducousso, Verification Manager of the Home Video Division, STMicroelectronics, France
- Jakob Engblom, PhD. Technical Marketing Manager - Simics Wind River, Sweden
- Albrecht Mayer, Senior Principal Emulation Systems and Tooling, Infineon, Germany