

TECHNICAL PROGRAM COMMITTEE

Anteneh Abbo, NXP, NL
El Mostapha Aboulhamid, University of Montreal, CA
Jacob Abraham, University of Texas at Austin, US
Antonio J. Acosta, University of Seville/IMSE, ES
Allon Adir, IBM Haifa Labs, IL
Rob Aitken, ARM, US
Zaid Al- Ars, TU Delft, NL
James Aldis, Texas Instruments, US
Bashir M. Al-Hashimi, University of Southampton, UK
Amara Amara, ISEP, FR
Giulio Antonini, University of L'Aquila, IT
David Atienza, Complutense, University of Madrid/DACYA, ES
Todd Austin, University of Michigan, US
Florence Azais, LIRMM, FR
Louis Baguena, Alcatel Alenia Space, FR
Iris Bahar, Brown University, US
Felice Balarin, Cadence, US
Sanjoy Baruah, University of North Carolina, US
Giovanni Basso, University of Pisa, IT
Twan Basten, TU Eindhoven, NL
Murat Becer, CLK Design Automation
Bernd Becker, Albert-Ludwigs-University, Freiburg, DE
Juergen Becker, University of Karlsruhe (TH), DE
Marc Belleville, CEA LETI, FR
Giovanni Beltrame, Politecnico di Milano, IT
Luca Benini, DEIS/University of Bologna, IT
Alfredo Benso, Politecnico di Torino, IT
Reinaldo Bergamaschi, IBM T J Watson Research Center, US
Michel Berkelaar, Magma, NL
Amine Bermak, Hong Kong Univ. of Science & Technology, HK
David Bernstein, IBM Haifa Labs, IL
Valeria Bertacco, University of Michigan, US
Koen Bertels, TU Delft, NL
Davide Bertozzi, DEIS/University of Bologna, IT
Swarup Bhunia, Case Western Reserve University, US
Armin Biere, Johannes Kepler University Linz, AT
Shawn Blanton, Carnegie Mellon University, US
Roderick Bloem, TU Graz, AT
Eduardo Boemo, Universidad Autonoma de Madrid, ES
Alessandro Bogliolo, University of Urbino, IT
Guy Bois, Ecole Polytechnique de Montreal, CA
Cristiana Bolchini, Politecnico di Milano, IT
Dominique Borrione, TIMA Laboratory, FR
Christos-Savvas Bouganis, Imperial College London, UK
Ian Broster, Rapita Systems Ltd
Joe Buck, Synopsys, US
Philippe Butel, MBDA, FR
Giorgio Buttazzo, Scuola Superiore S. Anna, IT
Gianpiero Cabodi, Politecnico di Torino, IT
Flavio Canavero, Politecnico di Torino, IT
Bernard Candaele, Thales Communications, FR
Andreas Cangellaris, University of Illinois, Urbana-Champaign, US
Jean-Lous Carbonero, STMicroelectronics, FR
Luca Carloni, UC Berkeley, US
Luigi Carro, UFRGS, BR
Sergio Casciaro, IFC-CNR, IT
Krishnendu Chakrabarty, Duke University, US
Samarjit Chakraborty, National University of Singapore, SG
Yao-Wen Chang, National Taiwan University, ROC
Abhijit Chatterjee Georgia Institute of Technology, US
Peter Cheung, Imperial College London, UK
Kiyong Choi, Seoul National University, KR
Alessandro Cimatti, ITC-irst, IT
Koen Lindstroem Claessen, TU Chalmers, SE
Pierre J M Cluitmans, TU Eindhoven, NL
Osvaldo Colavin, STMicroelectronics, IT
George Constantinides, Imperial College London, UK
Marcello Coppola, STMicroelectronics, FR
Jordi Cortadella, UP Catalunya, ES
Sorin Cotofana, TU Delft, NL
Al Crouch, Inovys, US
Paolo D'Abramo, Austriamicrosystems, AT
Gabriele D'Antona, Politecnico di Milano, IT
Antonio Manuel Da Cruz Serra, Instituto Superior Tecnico, IT
Jean-Luc Danger, GET-ENST / CNRS-LTCI, FR
Luca Daniel, Massachusetts Institute of Technology, US
Alain Darte, ENS Lyon, FR
Koen De Bosschere, University of Ghent, BE
Harmke De Groot, European Microsoft Innovation Centre, NL
Marco De Marinis, SensorDynamics AG, IT
Giovanni De Micheli, EPFL, Lausanne, CH
Alex Dean, North Carolina State University, US
Jean-Dominique Decotignie, CSEM, CH
Wim Dehaene, KU Leuven, BE
Andre Dehon, Caltech, US
Manuel Delgado-Restituto, Inst. of Microelectronics of Seville/CNM-CSIC, ES
Laurent Demeus, CISSOID, FR
Alper Demir, Koç University
Olivier Deprez, Texas Instruments, FR
Mario Diaz-Nava, STMicroelectronics, FR
John Dielissen, NXP, NL
Eva Dokladalova, CEA, FR
Rainer Dorsch, IBM Boeblingen, DE
Rolf Drechsler, University of Bremen, DE
Christian Drewes, Infineon Technologies, DE
Elena Dubrova, Royal Institute of Technology, SE
Surrendra, Dudani, Synopsys, US
Nikil Dutt, UC Irvine, US
Carl Ebeling, University of Washington, US
Petru Eles, Linkoping University, SE
Abe Elfadel, IBM T J Watson Research Center, US
Peeter Ellervee, TU Tallinn, EE
Francesco Faita, IFC-CNR, IT
Farzan Fallah, Fujitsu Labs of America, US
Luca Fanucci, University of Pisa, IT

Andrea Fedeli, STMicroelectronics, IT
 Peter Feldmann, IBM T J Watson Research Center, US
 Uwe Feldmann, Infineon Technologies, DE
 Francisco V Fernandez, CNM Seville, ES
 Fabrizio Ferrandi, Politecnico di Milano, IT
 Alberto Ferrari, PARADES, IT
 Carles Ferrer, Universitat Autònoma de Barcelona, ES
 Joan Figueras, UP Catalunya, ES
 Limor Fix, Intel Research, US
 Denis Flandres, Université Catholique de Lovain, BE
 Marie-Lise Flottes, LIRMM, FR
 Gerhard Fohler, TU Kaiserslautern, DE
 Harry Foster, Mentor Graphics, US
 Cecelia Franjo, Brunel University, UK
 Eby Friedman, University of Rochester, US
 Franco Fummi, University of Verona, IT
 Frederic Gaffiot, Ecole Centrale de Lyon, FR
 Rajesh Galivanche, Intel Corporation, US
 Maria Teresa Garcia Gonzales, Universidad Autónoma
 Metropolitana, ES
 Lovic Gauthier, Fukuoka Industry, Science & Tech.
 Foundation, JP
 Catherine Gebotys, University of Waterloo, CA
 Marc Geilen, TU Eindhoven, NL
 Sabih Gerez, Twente University, NL
 Joachim Gerlach, Robert Bosch GmbH, DE
 Andreas Gerstlauer, UC Irvine, US
 Romano Giannetti, Universidad Pontificia de Madrid, ES
 Holger Giese, University of Paderborn, DE
 Arnold Ginetti, Cadence Design Systems, FR
 Enrico Giunchiglia, DIST - University of Genova, IT
 Dimitris Gizopoulos, University of Piraeus, GR
 Manfred Glesner, TU Darmstadt, DE
 Michael Goessel, Potsdam University, DE
 Maya Gokhale, Los Alamos National Lab, US
 Joel Goossens, Université Libre de Bruxelles, BE
 Kees Goossens, NXP, NL
 Helmut Graeb, TU Munich, DE
 Miltos Grammatikakis, INESC-ID/IST, PT
 Cypryan Grassmann, Infineon Technologies, DE
 Jeff Gray, University of Alabama, Birmingham, US
 Patrick Groeneveld, Magma, NL
 Carlo Guardiani, PDF Solutions, IT
 Carlotta Guiducci, University of Bologna, IT
 Sylvain Guilley, GET-ENST / CNRS-LTCl, FR
 Aarti Gupta, NEC Labs America, US
 Rajiv Gupta, University of Arizona, US
 Sandeep Gupta, University of Southern California, US
 Soonhoi Ha, Seoul National University, KR
 Josef Haid, Infineon Technologies, DE
 Said Hamdioui, TU Delft, NL
 Ian Harris, UC Irvine, US
 Lars Hedrich, University of Frankfurt / Main, DE
 Christoph Heer, Infineon Technologies, DE
 Marc Heijligers, NXP, NL
 Ulrich Heinkel, TU Chemnitz, DE
 Sybille Hellebrand, University of Paderborn, DE
 Teun Hendriks, Embedded Systems Institute, NL
 Joerg Henkel, University of Karlsruhe, DE
 Tomas Henriksson, NXP, NL
 Tom Henzinger, EPFL Lausanne, CH
 Andreas Herkersdorf, TU Munich, DE
 Dwight Hill, Synopsys, US
 Xian-Long Hong, Tsinghua University, PRC
 Philippe Hoogvorst, GET-ENST / CNRS-LTCl, FR
 Nuno Horta, INESC-ID/IST, PT
 Michael S Hsiao, Virginia Tech, US
 Harry Hsieh, UC Riverside, US
 Alan Hu, University of British Columbia, CA
 Mike Hutton, Altera, US
 Paolo Ienne, EPFL Lausanne, CH
 Thomas Ifström, Robert Bosch GmbH, DE
 Andre Immink, NXP, NL
 Tohru Ishihara, Kyushu University, JP
 Valerie Issamy, INRIA, FR
 Andre Ivanov, University of British Columbia, CA
 Margarida Jacome, University of Texas at Austin, US
 Ahmed A Jerraya, TIMA Laboratory, FR
 Frank Johannes, TU Munich, DE
 Daniel Kaestner, Absint GmbH, DE
 Philippe Kajfasz, Thales Technology Centre Singapore
 (TTCS), SG
 Seiji Kajihara, Kyushu Institute of Technology, JP
 Timothy Kam, Intel, US
 Bozena Kaminska, Simon Fraser University, CA
 Mahmud Kandemir, Pennsylvania State University, US
 Ramesh Karri, Brooklyn Polytechnic University, US
 Gabor Karsai, Vanderbilt University, US
 Ashraf Kassim, National University of Singapore, SG
 Ryan Kastner, University of California, US
 Manolis Katevenis, FORTH and University of Crete, GR
 Marc-Olivier Killijain, CNRS – LAAS, FR
 Christoph Kirsch, University of Salzburg, AT
 Michael Kishinevsky, Intel, US
 Wolfgang Klingauf, TU Braunschweig, DE
 Tim Kogel, CoWare, BE
 Victor Kravets, IBM, US
 Dimitris Kritharidis, Intracom S.A., GR
 Thomas Kropf, Robert Bosch GmbH, DE
 Wido Kruijtzter, NXP Semiconductors, NL
 Andreas Kuehlmann, Cadence Berkeley Labs, US
 Tei-Wei Kuo, National Taiwan University, ROC
 Fadi Kurdahi, UC Irvine, US
 John Lach, University of Virginia, US
 Marcello Lajolo, NEC Labs America, US
 Christian Landrault, LIRMM, FR
 Peter Lange, Fraunhofer Inst. fuer Siliziumtechnologie, DE
 Erik Larsson, Linköping University, SE
 Luciano Lavagno, Politecnico di Torino, IT
 Yusuf Leblebici, EPFL Lausanne, CH
 Chen-Yi Lee, National Chiao Tung University, ROC
 Kangmin Lee, KAIST, KR
 Domine M W Leenaerts, NXP, NL
 Miriam Leeser, Northeastern University, US
 Rainer Leupers, RWTH Aachen University, DE
 Regis Leveugle, TIMA Laboratory, FR

Jens Lienig, TU Dresden, DE
Johan Lilius, Abo Akademi University, FI
Dake Liu, Linkoping University, SE
J C Lo, University of Rhode Island, US
Fabrizio Lombardi, Northeastern University, US
Wayne Luk, Imperial College London, UK
Patrick Lysaght, Xilinx, US
Roman Lysecky, University of Arizona, US
Jose Machado Da Silva, INESC, PT
Alberto Macii, Politecnico di Torino, IT
Yiorgios Makris, Yale University, US
Piero Malcovati, University of Pavia, IT
Philippe Manet, Université catholique de Louvain, BE
Alan Mantooth, University of Arkansas, US
Riccardo Mariani, Yogitech SPA, IT
Erik Jan Marinissen, NXP, NL
Igor Markov, University of Michigan, US
Joao Marques-Silva, University of Southampton, UK
Ewout Martens, KU Leuven, BE
Grant Martin, Tensilica, US
Guido Masera, Politecnico di Torino, IT
Yehia Massoud, Rice University, US
Peter M Maurer, Baylor University, US
Philippe Maurine, LIRMM, FR
Samy Meftali, Université de Lille, FR
Cecilia Metra, University of Bologna, IT
Pietro Michiardi, Institut Eurécom
Liviu Miclea, TU Cluj-Napoca
Salvador Mir, TIMA Laboratory, FR
Miguel Miranda, IMEC, BE
Massimo Mischi, TU Eindhoven, NL
Prabhat Mishra, University of Florida, US
Maria Carmen Molina, Universidad Complutense de Madrid, ES
Pieter Mosterman, The MathWorks Inc, US
Roland Mueller, Astrium GmbH, DE
Wolfgang Mueller, University of Paderborn, DE
Irin Munteanu, Computer Simulation Technology, DE
Rajeev Murgai, Fujitsu Laboratories, US
Smail Nair, University of Valenciennes, FR
Walid Najjar, UC Riverside, US
Vijaykrishnan Narayanan, Pennsylvania State University, US
Alessandra Nardi, Magma Design Automation, US
Leyla, Nazhandali, Virginia Tech, US
Wolfgang Nebel, Oldenburg University and OFFIS, DE
Gabriela Nicolescu, Ecole Polytechnique de Montreal, CA
Nicola Nicolici, McMaster University, CA
Spyridon Nikolaidis, Aristotle University of Thessaloniki, GR
Dimitris Nikolos, University of Patras, GR
Franc Novak, Jozef Stefan Institute, SL
Steven Nowick, Columbia University, US
Adrian Nunez, Syracuse University, US
Michael O'Boyle, University of Edinburgh, UK
Hermann Obermeier, Infineon Technologies, DE
Seda Ogrenic Memik, Northwestern University, US
Arlindo Oliveira, INESC-ID/IST, PT
Ian Oliver, Nokia, FI
Mauro Olivieri, University La Sapienza di Roma, IT
Alex Orailoglu, UC San Diego, US
Renaud Pacalet, GET-ENST / CNRS-LTCl, FR
Gianluca Palermo, Politecnico di Milano, IT
Vassilis Paliouras, University of Patras, GR
David Z Pan, The University of Texas at Austin, US
Preeti Panda, Indian Institute of Technology, Delhi, IN
Rajendran Panda, Freescale, US
Santosh Pande, Georgia Institute of Technology, US
Chris Papachristou, Case Western Reserve University, US
Marios Papaefthimiou, University of Michigan, US
Sri Parameswaran, University of New South Wales, AU
Antonios Paschalis, University of Athens, GR
Claudio Passerone, Politecnico di Torino, IT
Bipul C Paul, Toshiba American Research, US
Christian Paulus, Siemens, DE
Michael Payer, Infineon Technologies, DE
Carl Petry, Alcatel, BE
Paolo Pezzati, Cadence, FR
Stanislaw Piestrak, Politechnika Wroclawska, PL
Larry Pileggi, Carnegie Mellon University, US
Carl Pixley, Synopsys, US
Aleksandra Pizurica, University of Ghent, BE
Marco Platzner, University of Paderborn, DE
Dionisios Pnevmatikatos, TU Crete, GR
Frank Poehl, Infineon Technologies, DE
Vanni Poletto, STMicroelectronics, IT
Irith Pomeranz, Purdue University, US
Massimo Poncino, Politecnico di Torino, IT
Paul Pop, DTU, DK
Miodrag Potkonjak, UCLA, US
Fabien Pouget, Institut Eurécom
K M M Prabhu, Indian Institute of Technology, Madras, IN
Francis Presseccq, CNES, FR
Paolo Prinetto, Politecnico di Torino, IT
Peter Puschner, TU Vienna, AT
Anand Raghunathan, NEC Laboratories, US
Jaan Raik, TU Tallinn, EE
Srivaths Ravi, NEC Laboratories, US
Marc Renaudin, INPG, FR
Marta Rencz, Budapest University of Technology & Economics, HU
Michael Renovell, LIRMM, FR
Annette Reutter, Robert Bosch GmbH, DE
Pavel Ripka, Czech Technical University, CZ
Nikhil Rishiyur, Bluespec
Roberto Rivoir, FR
Guillermo Robles, University Carlos III of Madrid, ES
Angel Rodriguez-Vazquez, AnaFocus, ES
Pier Luigi Rolandi, NVM Design Platform, IT
Ronny Ronen, Intel, US
Frederic Rousseau, TIMA Laboratory, FR
Kaushik Roy, Purdue University, US
Jaijeet Roychowdhury, University of Minnesota, US
Antonio Rubio, UP Catalunya, ES
Adoracion Rueda, Seville U/IMSE-CNM, ES
Rob A Rutenbar, Carnegie Mellon University, US
Fabio Salice, Politecnico di Milano, IT

Christian Salzmann, BMW Group, DE
Pablo Sanchez, University of Cantabria, ES
Ingo Sandler, Royal Institute of Technology, SE
Miguel Santana, STMicroelectronics, FR
Sachin Sapatnekar, University of Minnesota, US
Sergio Saponara, University of Pisa, IT
Sebastian Sattler, Infineon Technologies, DE
Yiannakis Sazeides, University of Cyprus, CY
Alberto Scandurra, STMicroelectronics, IT
Tim Schattkowsky, University of Paderborn, DE
Wil Schilders, TU Eindhoven & NXP, NL
Eike Schmidt, ChipVision Design Systems, DE
Donatella Sciuto, Politecnico di Milano, IT
Lesley Shannon, University of British Columbia, CA
Tom Shiple, Synopsys, FR
Omid Shoaie, University of Tehran, IR
Cristina Silvano, Politecnico di Milano, IT
Luis Miguel Silveira, TU Lisbon/INESC, PT
Tajana Simunic, UC San Diego, US
Montek Singh, University of North Carolina, Chapel Hill,
US
Satnam Singh, Microsoft, US
Mustapha Slamani, IBM
Mark Smith, Kungliga Tekniska Hogskolan, SE
Ingemar Soderquist, Saab AB, SE
Mani Soma, University of Washington, US
Dinesh Somasekhar, Intel, US
Xiaoyu Song, Portland State University, US
Matteo Sonza Reorda, Politecnico di Torino, IT
Dimitrios Soudris, Thrace Democritus University, GR
Jonathan Sprinkle, UC Berkeley, US
Georgios Stamoulis, University of Thessaly, GR
Mircea Stan, University of Virginia, US
Liesbeth Steffens, NXP, NL
Per Stenstrom, TU Chalmers, SE
Bernd Straube, FhG IIS/EAS Dresden, DE
Guido Stromberg, Infineon Technologies, DE
Roberto Suaya, Mentor Graphics, FR
Myung Sunwoo, Ajou University, HK
Janos Sztipanovits, Vanderbilt University, US
Yutaka Tamiya, Fujitsu Labs, JP
Juergen Teich, University of Erlangen Nuremberg, DE
Paulo Teixeira, INESC-ID/IST, PT
Bernardo Tellini, University of Pisa, IT
Kris Tiri, Intel Corporation, US
Nigel Topham, University of Edinburgh, UK
Leonardo Tosi, Cevit Microelettronica, IT
Nur Touba, The University of Texas at Austin, US
Spiros Tragoudas, Southern Illinois University, Carbondale,
US
Stavros Tripakis, Cadence Berkeley Labs, US
Christian Trump, Siemens, DE
Jerzy Tyszer, TU Poznan, PL
Raimund Ubar, TU Tallinn, EE
Mihaela Ungureanu, Politechnica University of Bucharest,
HU
Frank Vahid, UC Riverside, US
Stefano Valle, STMicroelectronics, IT
Pieter van der Wolf, NXP, NL
Rob van Heijster, TNO Defence, NL
Sjir van Loo, NXP, NL
Hans van Someren, ACE Associated Compiler Experts, NL
Gerd Vandersteen, IMEC, BE
Hans Vandierendonck, University of Ghent, BE
Bart Vanrumste, KU Leuven, BE
Stamatis Vassiliadis, TU Delft, NL
Ranga R Vemuri, University of Cincinnati, US
Andreas Veneris, University of Toronto, CA
Serge Vernalde, IMEC, BE
Tiziano Villa, DIEGM - Udine University, IT
Eugenio Villar, University of Cantabria, ES
Jeroen Vleigen, Texas Instruments, FR
Klaus Waldschmidt, J W Goethe University, Frankfurt, DE
Piet Wambacq, IMEC, BE
Virginie Watine, Thales, FR
Matthias Weiss, NXP, NL
Chin-Long Wey, National Central University, ROC
Chris Wilson, Nusym Technology
Fabian Wolf, Volkswagen AG, DE
Christophe Wolinski, University of Rennes, FR
Cheng-Wen Wu, National Tsing Hua University, ROC
Hans-Joachim Wunderlich, University of Stuttgart, DE
Chen Xudong, National University of Singapore, SG
Alex Yakovlev, Newcastle University, UK
Sungjoo Yoo, Samsung Electronics, KR
Karen Yorav, IBM Haifa Labs, IL
Roberto Zafalon, STMicroelectronics, IT
Zhenhai Zhu, Cadence Berkeley Labs, US
Amir Zjajo, NXP, NL
Mark Zwolinski, University of Southampton, UK