



Conference and Exhibition: March 10-14, 2008 Munich, Germany

Call for Papers

Scope of the Event

The 11th DATE conference and exhibition is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. It puts strong emphasis on both ICs/SoCs, reconfigurable hardware and embedded systems, including embedded software.

Structure of the Event

The five-day event consists of a conference with plenary invited papers, regular papers, panels, hot-topic sessions, tutorials, workshops, two special focus days and a track for executives. The scientific conference is complemented by a commercial exhibition showing the state-of-the-art in design and test tools, methodologies, IP and design services, reconfigurable and other hardware platforms, embedded software, and (industrial) design experiences from different application domains, such as automotive, wireless, telecom and multimedia applications. The organisation of user group meetings, fringe meetings, a university booth, a PhD forum, vendor presentations and social events offers a wide variety of extra opportunities to meet and exchange information on relevant issues for the design and test community. Special space will also be allocated for EU-funded projects to show their results. More details are given on the website.

Areas of Interest

Within the scope of the conference, the main areas of interest are: embedded systems, design methodologies, CAD languages, algorithms and tools, testing of electronic circuits and systems, embedded software, applications design and industrial design experiences. Topics of interest include, but are not restricted to:

- System Design Methods, Algorithm and Tools
- Reconfigurable Computing
- System Level Specification and Modeling
- Innovative and Emerging Technologies, Systems and Applications
- System Synthesis and Optimization
- Simulation and Validation
- Design of Low Power Systems
- Power Estimation and Optimization
- Formal Verification
- Multi Processor and Network on Chip
- Microarchitectural and Architectural Design
- Architectural Synthesis
- Logic and Technology Dependent Synthesis for Deep-submicron Circuits
- Physical Design and Verification
- Analogue and Mixed A/D Systems
- Analogue and Mixed Signal Design, Symbolic Techniques
- Interconnect, EMC and Packaging Modeling
- Design and Application Case Studies
- System and Industrial Test
- BIST and Design for Testability
- Test Generation, Simulation and Diagnosis
- On-line Testing, Fault Tolerance and Reliability
- Testing of Analogue, Mixed-Signal, RF and Heterogeneous Circuits and Systems
- Defect-based Testing and Test of Regular Structures
- Real-time Systems and Middleware
- Compilers, Architectures, and Software Synthesis for Embedded Systems
- Model-based Design for Embedded Systems
- Hardware-Dependent Software for Embedded Systems
- Media and Signal Processing
- Wireless Communication and Networking
- Automotive Electronics and Software
- Dependable Embedded Systems
- Secure and Security Systems
- Industrial Design Methods
- Sensor Network
- MEMS and Nanotechnology

Submission of Papers

All papers have to be submitted electronically before September 9th, 2007, via the conference web page:

<http://www.date-conference.com/>

Papers can be submitted either for standard oral presentation or for interactive presentation.

Conference Secretariat

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