TECHNICAL PROGRAMME TOPIC CHAIRS

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Co-Chair: Andreas Gerstlauer
University of Texas at Austin, US

D2 System Design, High-Level Synthesis and Optimization
Chair: Andreas Herkersdorf
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Co-Chair: Nikil Dutt
University of California Irvine, US

D3 System Simulation and Validation
Chair: Elena Ioana Vatajelu
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University of Michigan, US

D4 Formal Methods and Verification
Chair: Christoph Scholl
University of Freiburg, DE
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DT5 Design and Test for Analog and Mixed-Signal Circuits and Systems, and MEMS
Chair: André Ivanov
University of British Columbia, CA
Co-Chair: Georges Gielen
Katholieke Universiteit Leuven, BE

D6 Power Modeling, Optimization and Low-Power Design
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D7 Network on Chip
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D8 Architectural and Microarchitectural Design
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D9 Temperature and Variability Aware System Design and Optimization
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UCM, ES

D10 Reconfigurable Computing
Chair: Fabrizio Ferrandi
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D11 Logical and Physical Analysis and Design
Chair: Luis Miguel Silveira
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D12 Emerging Technologies for Future Computing
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D13 Emerging Technologies for Future Memories
Chair: Jean-Michel Portal
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A1 Green Computing Systems
Chair: Andreas Burg
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A2 Communication, Consumer and Multimedia Systems
Chair: Steffen Paul
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A3 Automotive Systems and Smart Energy Systems
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A5 Secure Systems
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A6 Reliable and Reconfigurable Systems
Chair: Praveen Raghavan
IMEC, BE
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A7 Applications of Emerging Technologies
Chair: Andy Tyrrell
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Tsinghua University Beijing, CN

A8 Industrial Experiences Brief Papers
Chair: Raphaël David
CEA LIST, FR
Co-Chair: Eugenio Villar
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T1 Modeling and Mitigation of Defects, Faults, Variability, and Reliability
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T2 Test Generation, Simulation and Diagnosis
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Mentor, US
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T3 Design-for-Test, Test Infrastructures, Test Standards
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T4 System-Level Reliability Design, Analysis and On-line Test
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E1 Real-time, Networked, and Dependable Systems
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E3 Model-based Design and Verification for Embedded Systems
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E4 Embedded Software Architectures
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