

# Technical Programme Topic Chairs

## D1 System Specification and Modeling

**Chair:** Christian Haubelt

University of Rostock, DE

**Co-Chair:** Andy Pimentel

University of Amsterdam, NL

## D2 System Design, High-Level Synthesis and Optimization

**Chair:** Andreas Herkersdorf

Technische Universität München, DE

**Co-Chair:** Nikil Dutt

University of California Irvine, US

## D3 System Simulation and Validation

**Chair:** Elena Ioana Vatajelu

Politecnico di Torino, IT

**Co-Chair:** Valeria Bertacco

University of Michigan, US

## D4 Formal Methods and System Verification

**Chair:** Julien Schmaltz

Eindhoven University of Technology, NL

**Co-Chair:** Christoph Scholl

University of Freiburg, DE

## DT5 Design and Test for Analog and Mixed-Signal Systems and Circuits

**Chair:** André Ivanov

University of British Columbia, CA

**Co-Chair:** Helmut Graeb

Technische Universität München, DE

## D6 Emerging Technologies and Systems

**Chair:** Michael Niemier

University of Notre Dame, US

**Co-Chair:** Ian O'Connor

University of Lyon, FR

## D7 Power Modeling, Optimization and Low-Power Design

**Chair:** Alberto Macii

Politecnico di Torino, IT

**Co-Chair:** Naehyuck Chang

KAIST, KR

## D8 Network on Chip

**Chair:** Fabien Clermidy

CEA-Leti, FR

**Co-Chair:** Luca Carloni

Columbia, US

## D9 Architectural and Microarchitectural Design

**Chair:** Todd Austin

University of Michigan, US

**Co-Chair:** Cristina Silvano

Politecnico di Milano, IT

## D10 Temperature and Variability Aware Design and Optimization

**Chair:** Giovanni Ansaloni

Università della Svizzera Italiana, CH

**Co-Chair:** José Ayala

Complutense University of Madrid, ES

## D11 Reconfigurable Computing

**Chair:** Fabrizio Ferrandi

Politecnico di Milano, IT

**Co-Chair:** Ryan Kastner

University of California San Diego, US

## D12 Logical and Physical Analysis and Design

**Chair:** Patrick Groeneveld

Synopsys, US

**Co-Chair:** Luis Miguel Silveira

Inesc, PT

## A1 Green Computing Systems

**Chair:** Murali Annavaram

University of Southern California, US

**Co-Chair:** Andreas Burg

EPFL, CH

## A2 Communication, Consumer and Multimedia Systems

**Chair:** Sergio Saponara

University of Pisa, IT

**Co-Chair:** Steffen Paul

University of Bremen, DE

## A3 Automotive Systems and Smart Energy Systems

**Chair:** David Boyle

Imperial College London, UK

**Co-Chair:** Felix Reimann

Audi, DE

## A4 Ambient Intelligence and Low-Power Systems for Healthcare, Wellness and Assistive Technology

**Chair:** Elisabetta Farella

Fondazione Bruno Kessler - ICT center, IT

**Co-Chair:** Francisco Rincon

EPFL, CH

## A5 Secure Systems

**Chair:** Tim Güneysu

University of Bremen, DE

**Co-Chair:** Wieland Fischer

Infineon Technologies, DE

## A6 Reliable and Reconfigurable Systems

**Chair:** Praveen Raghavan

IMEC, BE

**Co-Chair:** Christian Weis

University of Kaiserslautern, DE

## A7 Industrial Experiences Brief Papers

**Chair:** Ahmed Jerraya

CEA Leti, FR

**Co-Chair:** Michael Nicolaidis

TIMA, FR

## T1 Defects, Faults, Variability and Reliability Analysis and Modeling

**Chair:** Rob Aitken

ARM, US

**Co-Chair:** Michel Renovell

LIRMM, FR

## T2 Test Generation, Simulation and Diagnosis

**Chair:** Bernd Becker

University of Freiburg, DE

**Co-Chair:** Wu-Tung Cheng

Mentor, US

## T3 Design-for-Test, Test Compression and Access

**Chair:** Sybille Hellebrand

University of Paderborn, DE

**Co-Chair:** Magdy Abadir

Freescale Semiconductor, Inc., US

## T4 On-Line Test, Fault Tolerance and Robust Systems

**Chair:** Fabrizio Lombardi

Northeastern University, US

**Co-Chair:** Cristiana Bolchini

Politecnico di Milano, IT

## DT5 Design and Test for Analog and Mixed-Signal Circuits and Systems

**Chair:** André Ivanov

University of British Columbia, CA

**Co-Chair:** Helmut Graeb

Technische Universität München, DE

## E1 Real-time, Networked, and Dependable Systems

**Chair:** Rodolfo Pellizzoni

University of Waterloo, CA

**Co-Chair:** Jan Reineke

Universität des Saarlands, DE

## E2 Compilers and Software Synthesis for Embedded Systems

**Chair:** Tulika Mitra

National University of Singapore, SG

**Co-Chair:** Frank Hannig

Friedrich-Alexander-Universität

Erlangen-Nürnberg, DE

## E3 Model-based Design and Verification for Embedded Systems

**Chair:** Linh Thi Xuan Phan

University of Pennsylvania, US

**Co-Chair:** Petru Eles

Linköping University, SE

## E4 Embedded Software Architectures

**Chair:** Marc Geilen

Eindhoven University of Technology, NL

**Co-Chair:** Frédéric Petrot

TIMA, Grenoble Institute of Technology, FR

## E5 Cyber-Physical Systems

**Chair:** Paul Pop

Technical University of Denmark, DK

**Co-Chair:** Tarek Abdelzaher

University of Illinois at Urbana Champaign,

US