

DATE Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the best papers. The selection is performed by the award committee.

The DATE 2016 best papers are:

D Track

UTILIZING MACROMODELS IN FLOATING RANDOM WALK BASED CAPACITANCE EXTRACTION

Wenjian Yu¹, Bolong Zhang¹, Chao Zhang¹, Haiquan Wang¹, Luca Daniel²
¹Tsinghua University, ²Massachusetts Institute of Technology (MIT)

A Track

OTEM: OPTIMIZED THERMAL AND ENERGY MANAGEMENT FOR HYBRID ELECTRICAL ENERGY STORAGE IN ELECTRIC VEHICLES

Korosh Vatanparvar and Mohammad Al Faruque
University of California, Irvine

T Track

MODELING FABRICATION NON-UNIFORMITY IN CHIP-SCALE SILICON PHOTONIC INTERCONNECTS

Mahdi Nikdast¹, Gabriela Nicolescu², Jelena Trajkovic³, Odile Liboiron-Ladouceur⁴
*¹Polytechnique Montréal and McGill University, ²Polytechnique Montréal, ³Concordia
University, ⁴McGill University*

E Track

PROBABILISTIC WCET ESTIMATION IN PRESENCE OF HARDWARE FOR MITIGATING THE IMPACT OF PERMANENT FAULTS

Damien Hardy¹, Isabelle Puaut¹, Yiannakis Sazeides²
¹University of Rennes 1/IRISA, ²University of Cyprus

Best Paper Award Nominations

D track

A HOLISTIC TRI-REGION MLC STT-RAM DESIGN WITH COMBINED PERFORMANCE, ENERGY, AND RELIABILITY OPTIMIZATIONS"

Wujie Wen¹, Mengjie Mao², Hai Li², Yiran Chen², Yukui Pei³, Ning Ge³

¹Florida International University, ²University of Pittsburgh, ³Tsinghua University

PARETO FRONT ANALOG LAYOUT PLACEMENT USING SATISFIABILITY MODULO THEORIES

Sherif Saif¹, Mohamed Dessouky², M. Watheq El-Kharashi³, Hazem Abbas⁴, Salwa Nassar¹

¹Electronics Research Institute, ²Mentor Graphics Corporation, ³Faculty of Engineering, Ain Shams University, ⁴Faculty of Media Engineering & Technology, GUC

A RECONFIGURABLE HETEROGENEOUS MULTICORE WITH A HOMOGENEOUS ISA

Jeckson Dellagostin Souza¹, Luigi Carro², Mateus Beck Rutzig³, Antonio Carlos Schneider Beck Filho¹

¹Universidade Federal do Rio Grande do Sul, ²Universidade Federal do Rio Grande do Sul (UFRGS), ³Universidade Federal de Santa Maria

BUFFERED COMPARES: EXCAVATING THE HIDDEN PARALLELISM INSIDE DRAM ARCHITECTURES WITH LIGHTWEIGHT LOGIC"

Jinho Lee, Jung Ho Ahn, Kiyoungh Choi

Seoul National University

BUILT-IN TEST OF MILLIMETER-WAVE CIRCUITS BASED ON NON-INTRUSIVE SENSORS

Athanasios Dimakos¹, Haralampos-G. Stratigopoulos², Alexandre Siligaris³, Salvador Mir¹, Emeric De Foucauld³

¹Université Grenoble Alpes, CNRS, TIMA, ²Sorbonne Universités, UPMC, ³CEA-Leti

FORMAL VERIFICATION OF INTEGER MULTIPLIERS BY COMBINING GRÖBNER BASIS WITH LOGIC REDUCTION

Amr Sayed Ahmed¹, Daniel Grosse¹, Ulrich Kühne¹, Mathias Soeken¹, Rolf Drechsler²

¹University of Bremen, ²University of Bremen/DFKI

A track

TOTAL: TRNG ON-THE-FLY TESTING FOR ATTACK DETECTION USING LIGHTWEIGHT HARDWARE

Bohan Yang¹, Vladimir Rozic¹, Nele Mentens¹, Wim Dehaene², Ingrid Verbauwhede¹

¹KU Leuven, ²KU Leuven and IMEC

T track

PRE-BOND TESTING OF THE SILICON INTERPOSER IN 2.5 D ICs

Ran Wang¹, Zipeng Li¹, Sukeshwar Kannan², Krishnendu Chakrabarty¹
¹Duke University, ²Global Foundries Inc.

INEXACT DESIGNS FOR APPROXIMATE LOW POWER ADDITION BY CELL REPLACEMENT

Haider A.F. Almurib¹, Nandha Kumar Thulasiraman¹, Fabrizio Lombardi²
¹The University of Nottingham, ²Northeastern University