

technical programme topic chairs

System Specification and Modelling

Andy Pimentel
Amsterdam U, NL
Dominique Borrione
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System Co-Design, Synthesis and Optimisation

Samarjit Chakraborty
TU Munich, DE
Luciano Lavagno
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Simulation and Validation

Valeria Bertacco
U of Michigan, US
Franco Fummi
Verona U, IT

Design of Low Power Systems

Alberto Macii
Politecnico di Torino, IT
Tudor Murgan
Infineon Technologies, DE

Power Estimation and Optimisation

Joerg Henkel
Karlsruhe U, DE
Massimo Poncino
Politecnico di Torino, IT

Emerging Technologies, Systems and Applications

Yuan Xie
Penn State U, US
Sanjukta Bhanja
U of South Florida, US

Formal Methods and Verification

Wolfgang Kunz
TU Kaiserslautern, DE
Gianpiero Cabodi
Politecnico di Torino, IT

Network on Chip

Davide Bertozzi
U of Ferrara, IT
David Atienza
EPF Lausanne, CH

Architectural and Microarchitectural Design

Laura Pozzi
Lugano U, CH
Sami Yehia
THALES Research and Technology, FR

Architectural and High-Level Synthesis

Paolo Ienne
EPF Lausanne, CH
Philippe Coussy
U de Bretagne-Sud, FR

Reconfigurable Computing

Patrick Lysaght
Xilinx, US
Fabrizio Ferrandi
Politecnico di Milano, IT

Logic and Technology Dependent Synthesis for Deep-Submicron Circuits

Michel Berkelaar
TU Delft, NL
Iris Bahar
Brown U, US

Physical Design and Verification

Igor Markov
U of Michigan, US
Azadeh Davoodi
U of Wisconsin, US

Analogue and Mixed-Signal Systems and Circuits

Christoph Grimm
TU Wien, AT
Helmut Graeb
TU Munich, DE

Interconnect, EMC, EMD and Packaging Modelling

Wil Schilders
NXP, NL
Tom Dhaene
Ghent U, BE

Computing Systems

Wilfried Verachtert
IMEC, BE
Krisztian Flautner
ARM, UK

Communication, Consumer and Multimedia Systems

Frank Kienle
TU Kaiserslautern, DE
Matthias Sauer
Infineon Technologies, DE

Transportation Systems

Marco Di Natale
Scuola Superiore Sant'Anna, IT
Christian Sebeke
Bosch, DE

Medical and Healthcare Systems

Wolfgang Eberle
IMEC, BE
Carlotta Guiducci
EPF Lausanne, CH

Energy Generation, Recovery and Management Systems

Davide Brunelli
Trento U, IT
Salvatore Rinaudo
STMicroelectronics, IT

Secure, Dependable and Adaptive Systems

Ingrid Verbauwhede
KU Leuven, BE
Jerome Quevremont
Thales, FR

Test for Defects, Variability and Reliability

Sandip Kundu
Massachusetts U, US
Bram Kruseman
NXP, NL

Test for Mixed-Signal, Analogue, RF, MEMS

Hans Kerkhoff
Twente U, NL
Abhijit Chatterjee
Georgia Tech, US

Test Access, Design-for-Test, Test Compression, System Test

Sandeep Kumar Goel
TSMC, US
Sybill Hellebrand
Paderborn U, DE

On-Line Testing and Fault Tolerance

Dimitris Gizopoulos
Piraeus U, GR
Cecilia Metra
Bologna U, IT

Real-time, Networked and Dependable Systems

Luis Alemda
Aveiro U, PT
Peter Puschner
TU Wien, AT

Compilers and Code Generation for Embedded Systems; Software-Centric System Design Exploration

Bjoern Franke
Edinburgh U, UK
Heiko Falk
TU Dortmund, DE

Model-Based Design and Verification for Embedded Systems

Christoph Kirsch
Salzburg U, AT
Rupak Majumdar
UCLA, US

Embedded Software Architectures and Principles; Software for MPSoC, Multi-/Many-Core and GPU-Based Systems

Oliver Bringmann
FZI, DE
Gabriela Nicolescu
Polytechnique Montreal, CA