foreword



Dear Colleague

Welcome to DATE-2009, the 12th anniversary of the Design, Automation and Test in Europe Conference. DATE is a world-leading event on electronic systems design and industrial applications. This year's event is marked by an exciting development which is DATE and ARTEMISIA Association for R&D actors in Advanced Research and Technology of EMbedded Intelligence and Systems have joined forces to stage joint event in Nice. ARTEMISIA is the main industry-research association in Europe focusing on embedded systems and has more than 150 members including key European companies such as Airbus, Thales, Daimler, Finmeccanica, Philips, Siemens, STMicroelectronics, NXP, Infineon and many others. Artemisia, together with the European Commission and EU Member States have formed the ARTEMIS joint undertaking, a multi-billion € public-private partnership for collaborative research and



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development in embedded systems. We are confident all stakeholders in our industry including research, design and management will benefit from the great networking opportunities offered by the co-location of DATE and ARTEMISA.

With the help of more than 400 members of the technical programme committee, who dedicated their time to review the record number of 965 submissions, DATE has prepared an exciting technical programme. This year the most popular topics are Network on Chip, Design of Low Power Systems, Secure Embedded Implementations, Application of Reconfigurable Systems, On-Line Testing and Fault Tolerance, and Real-Time, Networked and Dependable Systems. Particularly relevant, this year DATE has received a record number of submissions in the Industrial Application Track, increasing by 30% on last year, confirming DATE is the world leading conference on system design and industrial applications from hardware to software.

Papers are organized in 4 major areas:

D- Design Methods, Tools, Algorithms and Languages, **A**- Application Design **T**- Test Methods, Tools and Innovative Experience, **E**- Embedded Software This year the conference will be held in France, at the Acropolis in Nice and will span an entire working week starting on Monday April 20, with tutorials, and ending on Friday April 24 with Friday workshops. The conference program includes 226 papers organised in 8 parallel tracks, with 15 special sessions. Additionally, there are 53 Interactive Presentations which are organised into 5 IP sessions.

The plenary keynote speakers on April 21st are Mike Muller of ARM, to consider whether anything has changed in electronic design since 1983, and Joseph Sifakis of CNRS/Verimag to discuss embedded systems design – scientific challenges and work directions. On the same day, the Executive Track offers a series of business panels with executives discussing hot topics in design: "FPGA Developments: What's New?", A Progress Report on the Move to 32nm" and "Funding New Ventures: To emphasise that DATE is the major event for the designers, DATE 09 features invited sessions where Europe's renowned consumer industry presents its best designs and design practices.

DATE offers two specific days related to special themes: on April 22nd sessions will focus on SoC Development Strategies. On April 23rd sessions will focus on Multicore Applications. Finally, DATE offers a comprehensive overview of commercial design and verification tools in its large exhibition and abundant networking possibilities, including fringe meetings and a memorable social party with bands and dancing on Wednesday evening.

DATE 09 General Chair

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DATE 09 Programme Chair

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