

Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the most outstanding papers of the previous year's conference. The selection is performed by an award committee, based on the results of the reviewing process, the quality of the final paper and the quality of the presentation.

The paper selected as the most outstanding in Track D is:

Run-Time System for an Extensible Embedded Processor with Dynamic Instruction Set

by L Bauer, M Shafique, S Kreutz and Joerg Henkel, University of Karlsruhe, Germany

The paper selected as the most outstanding in Track A is:

Power Balanced Gates Insensitive to Routing Capacitance Mismatch

by K J Kulikowski, V Venkatarama, Z Wang and A Taubin, Boston University, USA

The paper selected as the most outstanding in Track E is:

Retargetable Code Optimization for Predicated Execution

*by M Hohenauer, R Leupers, G Ascheid and H Meyr, RWTH Aachen University, Germany
and
G Bette, Associated Compiler Experts, The Netherlands
and
B Singh, NXP Semiconductors, The Netherlands*

The paper selected as the most outstanding IP is:

Analysis and Optimization of the Recessed Probe Launch for High Frequency Measurements of PCB Interconnects

*by R Rimolo-Donadio and C Schuster, TU Hamburg-Harburg, Germany
and
X Gu, Y H Kwark and M B Ritter, IBM Thomas J Watson Research Center, USA*

Congratulations to the winners!