

Technical Program Chairs



PROGRAM CHAIR
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University of Dortmund, D



VICE PROGRAM CHAIR
Carlos Delgado-Kloos
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PAST PROGRAM CHAIR
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TIMA, Grenoble, F



SYSTEM DESIGN
Luciano Lavagno
U Udine, IT



ANALOG & MIXED A/D
Francisco Fernandez
U Seville, ES



LOW POWER
Ingrid Verbauwhede
UCLA, USA



DESIGN REUSE, IP
Mark Genoe
Alcatel, B



EMBEDDED SYSTEMS
Jef van Meerbergen
Philips, NL



MODELLING & HDL
Wolfgang Nebel
U Oldenburg, D



SIMULATION
Peter Schwarz
FhG IIS/EAS Dresden, D



HW/SW CODESIGN
Rolf Ernst
TU Braunschweig, D



ARCHITECTURAL
SYNTHESIS
Reinaldo Bergamashi
IBM, USA



LOGIC & FSM SYNTHESIS
Bill Lin
UC San Diego, USA



PHYSICAL DESIGN
Toshiro Akino
Kinki U, JP



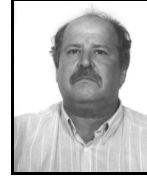
INTERCONNECT & MIXED
SIGNAL
Kurt Antreich
TU Munchen, D



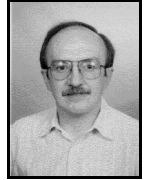
FORMAL VERIFICATION
Paolo Camurati
Politecnico di Torino, IT



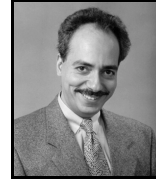
DEFECT ORIENTED TEST
Joan Figueras
UPC, ES



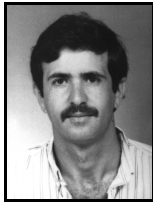
TEST GENERATION
Christian Landrault
LIRMM, F



DFT & BIST
Paolo Prinetto
P Torino, IT



TESTING CORES &
SYSTEMS
Yervant Zorian
LogicVision, USA



ON-LINE TESTING
Michael Nicolaidis
TIMA Grenoble, F

Vendors Committee

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Larry Eberle Synopsys, USA

Hein van der Wildt Sagantec, ISR

Herman Beke Frontier Design, B

Peter Denyer Sun Microsystems, USA

Catherine Weiss Cadence, D

Jeremy Kenyon EDA, UK

Andrew Porter EDA, UK