

# Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the most outstanding papers of previous year's conference. The selection is based on the results of the reviewing process and the voting by the conference participants.

The paper selected as the most outstanding in the field of CAD is:

## **Symbolic Functional Vector Generation for VHDL Specifications**

*by Fabrizio Ferrandi, Luca Gerli, and Donatella Sciuto of Politecnico di Milano, Italy  
and Franco Fummi of Università di Verona, Italy.*

The authors present a methodology for functional verification of VHDL specifications based on functional test vector generation using a very precise behavioral error model and a controllability metric for coverage evaluation. Experimental results show that high coverage is obtained in addition to detailed identification of those parts of the code that may include design errors.

The paper selected as the most outstanding in the field of Test is:

## **A CAD Framework for Generating Self-Checking Multipliers Based on Residue Codes**

*by I. Alzaher Noufal and M. Nicolaidis of TIMA, France.*

The authors present a set of tools for automatic generation of self-checking multipliers using residue arithmetic codes. The results combine reduced hardware overhead with fault security for a wide variety of multiplier structures and show that the residue based scheme is superior for large multipliers.

**Congratulations to the winners!**