

Technical Programme Topic Chairs

D1 System Specification and Modeling

Chair: Ingo Sander

KTH Royal Institute of Technology, SE

Co-Chair: Frederic Mallet

University of Nice Sophia Antipolis, FR

D2 System Design, High-Level Synthesis, and Optimization

Chair: Yuko Hara-Azumi

Tokyo Institute of Technology, JP

Co-Chair: Philippe Coussy

Université de Bretagne-Sud, FR

D3 System Simulation and Validation

Chair: Graziano Pravadelli

Department of Computer Science, University of Verona, IT

Co-Chair: Avi Ziv

IBM Research, IL

D4 Formal Methods and Verification

Chair: Armin Biere

Johannes Kepler University Linz, AT

Co-Chair: Alessandro Cimatti

Fondazione Bruno Kessler, IT

DT5 Design and Test for Analog and Mixed-Signal Circuits and Systems, and MEMS

Chair: Georges Gielen

KU Leuven, BE

Co-Chair: Francisco V. Fernandez

IMSE-CNM, CSIC and University Sevilla, ES

DT6 Design and Test of Secure Systems

Chair: Ilia Polian

University of Stuttgart, DE

Co-Chair: Lejla Batina

Radboud University Nijmegen, NL

D7 Network on Chip

Chair: Sébastien Le Beux

Lyon Institute of Nanotechnology, FR

Co-Chair: Masoud Daneshtalab

KTH Royal Institute of Technology, SE

D8 Architectural and Microarchitectural Design

Chair: Francisco Cazorla

BSC, ES

Co-Chair: Olivier Sentieys

INRIA, FR

D9 Power Modeling, Low-Power Design, and Power-Aware Optimization

Chair: Naehyuck Chang

KAIST, KR

Co-Chair: Andrea Calimera

Politecnico di Torino, IT

D10 Temperature and Variability Aware System Design and Optimization

Chair: Jose L. Ayala

Complutense University of Madrid, ES

Co-Chair: Qinru Qiu

Syracuse University, US

D11 Reconfigurable Computing

Chair: Philip Brisk

University of California, Riverside, US

Co-Chair: Suhaib A. Fahmy

University of Warwick, GB

D12 Logical and Physical Analysis and Design

Chair: Tiziano Villa

Dipartimento d'Informatica, Università di Verona, IT

Co-Chair: Elena Dubrova

Royal Institute of Technology - KTH, SE

D13 Emerging Design Technologies for Future Computing

Chair: Aida Todri-Sanial

CNRS-LIRMM/University of Montpellier, FR

Co-Chair: Subhasish Mitra

Stanford University, US

D14 Emerging Design Technologies for Future Memories

Chair: Jean-Michel Portal

Aix-Marseille University, FR

Co-Chair: Pierre-Emmanuel Gaillardon

University of Utah, US

A1 Power-efficient and Sustainable Computing

Chair: Muhammad Shafique

TU Wien, AT

Co-Chair: Baris Aksanli

San Diego State University, US

A2 Robotics and Industry 4.0

Chair: Paolo Fiorini

Università di Verona, IT

Co-Chair: Torsten Kroeger

Karlsruhe Institute of Technology, DE

A3 Automotive Systems and Smart Energy Systems

Chair: Davide brunelli

Università di Trento, IT

Co-Chair: Sebastian Steinhorst

TU Munich, DE

A4 Augmented Living and Personalized Healthcare

Chair: Theocharis Theocharides

University of Cyprus, CY

Co-Chair: Elisabetta Farella

Fondazione Bruno Kessler (FBK), IT

A5 Secure Systems, Circuits, and Architectures

Chair: Ingrid Verbauwhede

KU Leuven, BE

Co-Chair: Lionel Torres

Université de Montpellier, FR

A6 Self-adaptive and Learning Systems

Chair: Antonio Miele

Politecnico di Milano, IT

Co-Chair: Gilles Sassatelli

Université de Montpellier, FR

A7 Applications of Emerging Technologies

Chair: Andy Tyrrell

University of York, GB

Co-Chair: Yu Wang

Nanjing University of Posts and

Telecommunications, CN

A8 Industrial Experiences Brief Papers

Chair: Fabien Clermidy

CEA-Leti, FR

Co-Chair: Norbert Wehn

TU Kaiserslautern, DE

T1 Modeling and Mitigation of Defects, Faults, Variability, and Reliability

Chair: Said Hamdioui

TU Delft, NL

Co-Chair: Jose Pineda

NXP Semiconductors, NL

T2 Test Generation, Test Architectures, Design for Test, and Diagnosis

Chair: Patrick Girard

LIRMM, FR

Co-Chair: Bernd Becker

University of Freiburg, DE

T3 Microarchitecture-Level Dependability

Chair: Jaume Abella

BSC, ES

Co-Chair: Ramon Canal

UPC, ES

T4 System-Level Dependability

Chair: Maria K. Michael

Electrical and Computer Engineering & KIOS

Center of Excellence / University of Cyprus, CY

Co-Chair: Georgios Karakonstantis

Queen's University Belfast, GB

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Radboud University Nijmegen, NL

E1 Real-time and Dependable Systems

Chair: Kai Lampka

Electrobit Automotive GmbH, DE

Co-Chair: Dionisio de Niz

Carnegie Mellon University, US

E2 Embedded Systems for Deep Learning

Chair: Akash Kumar

TU Dresden, DE

Co-Chair: Sander Stuijk

Eindhoven University of Technology, NL

E3 Model-Based Design, Verification and Security for Embedded Systems

Chair: Ylies Falcone

University of Grenoble Alpes, Inria, FR

Co-Chair: Todd Austin

University of Michigan, US

E4 Embedded Software Architecture, Compilers and Tool Chains

Chair: Borzoo Bonakdarpour

Iowa State University, US

Co-Chair: Eli Bozorgzadeh

University of California, Irvine, US

E5 Cyber-Physical Systems Design

Chair: Shiyan Hu

Michigan Technological University, US

Co-Chair: Davide Quaglia

Università di Verona, IT