

Technical Programme Committee

Magdy Abadir, Independent, US

Tarek Abdelzaher, University of Illinois at Urbana Champaign, US

Jaume Abella, Barcelona Supercomputing Center, ES

Jacob Abraham, University of Texas at Austin, US

Nicola Acito, Direzione Studi Accademia Navale , IT

Antonio Acosta, Univ. of Seville/IMSE, ES

Robert Aitken, ARM, US

Bartomeu Alorda, Illes Balears University, ES

Lorena Anghel, TIMA, FR

Federico Angiolini, iNoCs, CH

Murali Annavaram, University of Southern California, US

Karl-Erik Arzen, Lund University, SE

Todd Austin, University of Michigan, US

Amer Baghdadi, TELECOM Bretagne, FR

R. Iris Bahar, School of Engineering, Brown University, US

Manuel Barragan, TIMA Laboratory, FR

Alberto A. Barrio del, Universidad Complutense de Madrid: UCM, ES

Sandro Bartolini, University of Siena, IT

Iain Bate, University of York, GB

Lars Bauer, KIT, DE

Juergen Becker, KIT - Karlsruher Institut für Technologie, DE

Bernd Becker, University of Freiburg, DE

Tobias Becker, Maxeler Technologies, GB

Edith Beigne, CEA-LETI Minatec, FR

Luca Benini, ETHZ, CH

Saddek Bensalem, Université Joseph Fourier, FR

Mladen Berekovic, TU Braunschweig, DE

Valeria Bertacco, University of Michigan, US

Marko Bertogna, University of Modena, IT

Swarup Bhunia, Case Western Reserve University, US

Armin Biere, Universitaet Linz, AT

Sounil Biswas, NVIDIA, US

Per Bjesse, Synopsys, IE

Michaela Blott, XILINX, IE

Cristiana Bolchini, Politecnico di Milano, IT

Christos Bouganis, Imperial College, GB

David Boyle, Imperial College London, GB

Elaheh Bozorgzadeh, University of California, Irvine, US

Arkadi Bramnik, INTEL, US

Oliver Bringmann, FZI / University of Tuebingen, DE

Philip Brisk, University of California, Riverside, US

Andreas Burg, EPFL, CH

Árpád Bürmén, University of Ljubljana, SI

Gianpiero Cabodi, Politecnico di Torino, IT

Andrea Calimera, Politecnico di Torino, IT

Luca Carloni, Columbia University, US

Jeronimo Castrillon, TU Dresden, Germany, DE

Francisco J. Cazorla, Barcelona Supercomputing Center, ES

Krishnendu Chakrabarty, Duke University, US

Naehyuck Chang, Korea Advanced Institute of Science and Technology (KAIST), KR

Mingsong Chen, East China Normal University, CN

Wu-Tung Cheng, Mentor Graphics, US

Alessandro Cimatti, FBK-irst (Fondazione Bruno Kessler - Center for Information technology), IT

Valentina Ciriani, University of Milano, IT

Fabien Clermidy, CEA-LETI, FR

Marcello Coppola, STMicroelectronics, FR

Philippe Coussy, Universite de Bretagne Sud / Lab-STICC, FR

Liliana Cucu, INRIA, FR

Masoud Daneshtalab, university of Turku, FI

Luca Daniel, Massachusetts Institute of Technology, US

Raphaël David, CEA LIST, FR

Azadeh Davoodi, University of Wisconsin - Madison, US

Florent de Dinechin, INSA de Lyon, FR

Dionisio de Niz, Carnegie Mellon University, US

Jean-Dominique Decotignie, EPFL, CH

Catherine Dehollain, EPFL, CH

Giuseppe Desoli, STMicroelectronics, IT

Georgios Dimitrakopoulos, Democritus University of Thrace (DUTH), GR

Gero Dittmann, IBM Research, CH

Marco Domenico Santambrogio, Polimi, IT

Christian Drewes, Intel Mobile Communications GmbH, DE

Elena Dubrova, Royal Institute of Technology - KTH, SE

Günhan Dündar, Bo?aziçi University, TR

Nikil Dutt, University of California, Irvine, US

Jennifer Dworak, Southern Methodist University, US

Petri Eles, Linköping University, SE

Piet Engelke, Infineon, DE

Adrian Evans, iRoC Technologies, FR

Suhaib A. Fahmy, University of Warwick, GB

Luca Fanucci, University of Pisa, IT

Elisabetta Farella, Fondazione Bruno Kessler, IT

Fabrizio Ferrandi, Politecnico di Milano, IT

Leandro Fiorin, IBM Research, NL

Wieland Fischer, Infineon, DE

Viktor Fischer, Laboratoire Hubert Curien, FR

Josè Flich, Universidad Politecnica de Valencia, ES

William Fornaciari, Politecnico di Milano, IT

Aurélien Francillon, EURECOM, FR

Pierre-Emmanuel Gaillardon, EPFL, CH

Alberto Garcia-Ortiz, Univ. Bremen, DE

Marc Geilen, TU Eindhoven, NL

Andreas Gerstlauer, University of Texas at Austin, US

Tuhin Ghosh Neogi , GlobalFoundries, US

Georges Gielen, Katholieke Universiteit Leuven, BE

Bastien Giraud, CEA-LETI, Minatec, FR

Alain Girault, INRIA, FR

Dimitris Gizopoulos, University of Athens, Gr

Michael Glaß, University of Erlangen, DE

Kees Goossens, Eindhoven Univ. of Technology, NL

Helmut Graeb, Technische Universität München, DE

Paul Gratz, Texas A&M University, US

Christoph Grimm, TU Vienna, AT

Patrick Groeneveld, Synopsys, US

Kim Grüttner, OFFIS - Institute for Information Technology, DE

Tim Güneysu, University of Bremen, DE

Soonhoi Ha, Seoul National University, KR

Sebastian Hack, University of Saarland, DE

Timo Hämäläinen, Tampere University of Technology, FI

Said Hamdioui, Delft University of Technology, NL

Yinhe Han, Chinese Academy of Science, CN

Jie Han, University of Alberta, CA

Frank Hannig, Friedrich-Alexander-Universität Erlangen-Nürnberg, DE
Andreas Hansson, ARM Ltd, GB
Yuko Hara-Azumi, Tokyo Institute of Technology, JP
Peter Harrod, ARM Ltd, GB
Christian Haubelt, University of Rostock, DE
Lars Hedrich, University of Frankfurt, DE
Sybille Hellebrand, University of Paderborn, DE
Jörg Henkel, Karlsruhe Institute of Technology, DE
Andreas Herkersdorf, TU München, DE
Shaahin Hessabi, Sharif University of Technology, IR
Marijn Heule, The University of Texas at Austin, US
Tsung-Yi Ho, National Cheng Kung University, TW
Nuno Horta, Instituto de Telecomunicações, Instituto Superior Técnico - TU Lisbon, PT
Shiyan Hu, Michigan Technological University, US
Michael Hübner, Ruhr-U Bochum, DE
Huynh Phung Huynh, A*STAR Institute of High Performance Computing, SG
Andre Ivanov, UBC, CA
Ravi Iyer, Intel, US
Viacheslav Izosimov, The Royal Institute of Technology, SE
Jorn W. Janneck, Lund University, SE
Maksim Jenihhin, Tallinn University of Technology, EE
Ahmed Jerraya, CEA Leti, FR
Ryan Kastner, University of California San Diego, US
Ronan Keryell, Xilinx, IE
John Kim, KAIST, KR
Soontae Kim, KAIST, KR
Jungsoo Kim, Samsung Electronics, KR
Jae-joon Kim, POSTECH, KR
Raimund Kirner, University of Hertfordshire, GB
Veit B. Kleeberger, Infineon Technologies AG, DE
Dirk Koch, University of Manchester, GB
Bram Kruseman, NXP Semiconductors, NL
Akash Kumar, Electrical and Computer Engineering, National University of Singapore, SG
Kai Lampka, Uppsala University, SE
Luciano Lavagno, Politecnico di Torino, IT
Sébastien Le Beux, Lyon Institute of Nanotechnology, FR
Benjamin C. Lee, Duke University, US
Kuen-Jong Lee, National Cheng Kung University, TW
Gildas Leger, IMSE-CNM-CSIC, ES
Benjamin Lesage, University of York, GB
Florian Letombe, Synopsys, FR
Huawei Li, Institute of Computing Technology, CAS, CN
Hai Li, Dept. of Electrical and Computer Engineering University of Pittsburgh, US
Yun (eric) Liang, Peking University, CN
Jens Lienig, Technical University of Dresden, DE
(Mark) Po-Hung Lin, National Chung Cheng University, TW
Eike Linn, RWTH Aachen, DE
Giuseppe Lipari, Scuola Superiore Sant'Anna, IT
Fabrizio Lombardi, Northeastern University, US
Marisa Lopez-Vallejo, UPM, ES
Enno Luebbers, Intel Open Lab Munich, DE
Martin Lukasiewycz, TUM CREATE, SG
Roman Lysecky, University of Arizona, US
Alberto Macii, Politecnico di Torino, IT
Jan Madsen, Technical University of Denmark, DK
Roel Maes, Intrinsic-ID, NL
Frédéric Mallet, Université Nice Sophia Antipolis, FR
Stefan Mangard, TU Graz, AT
Diana Marculescu, Carnegie Mellon University, US
Igor Markov, University of Michigan, US
Andrea Marongiu, University of Bologna, IT
Guido Masera, Politecnico di Torino, IT
Dmitri Maslov, National Science Foundation, US
Hiroki Matsutani, Keio University, JP
Emil Matus, Technische Universität Dresden, DE
Dorin Maxim, ISEP, PT
Marcel Medwed, NXP Semiconductors, AT
Filippo Melzani, STMicroelectronics, IT
Seda Memik, Northwestern University, US
Maria Michael, University of Cyprus, CY
Tulika Mitra, National University of Singapore, SG
Subhasish Mitra, Stanford University, US
Kartik Mohanram, University of Pittsburgh, US
José Monteiro, INESC-ID / IST, TU Lisbon, PT
Ronny Morad, IBM Research - Haifa, IL
Dominique MORCHE, CEA-LETI, FR
Orlando Moreira, Ericsson, NL
Srinivasan Murali, SmartCardia, CH
Onur Mutlu, Carnegie Mellon University, US
Hiroshi Nakamura, Graduate School of Information Science and Technology, the University of Tokyo, JP
Alberto Nannarelli, Technical University of Denmark, DK
Pritish Narayanan, IBM Research Almaden, US
Vijaykrishnan Narayanan, Pennsylvania State University, US
Smail Niar, University of Valenciennes, FR
Michael Nicolaïdis, TIMA, FR
Nicola Nicolici, McMaster University, CA
Michael Niemier, University Of Notre Dame, US
Steven Nowick, Columbia University, US
Ian O'Connor, Lyon Institute of Nanotechnology, FR
Maire O'Neill, Queen's University Belfast, GB
Fabian Oboril, KIT, DE
Umit Ogras, Arizona State University, US
Frank Oppenheimer, OFFIS e. V., DE
David Oswald, University of Birmingham, GB
Marco Ottavi, University of Rome "Tor Vergata", IT
John O'leary, Intel Corporation, US
Gianluca Palermo, Politecnico di Milano, IT
Sri Parameswaran, UNSW, AU
Steffen Paul, University Bremen, DE
Rodolfo Pellizzoni, University of Waterloo, CA
Sven Peyer, IBM, DE
Linh Thi Xuan Phan, University of Pennsylvania, US
Laurence Pierre, TIMA, FR
Christian Pilato, Columbia University, US
Pit Pillatsch, UC Berkeley, US
Andy Pimentel, University of Amsterdam, NL
Marco Platzner, University of Paderborn, DE
Christian Plessl, University of Paderborn, DE
Dionisios Pnevmatikatos, Technical University of Crete, GR

Ilia Polian, University of Passau, DE
Florian Pöhlbauer, Virtual Vehicle, AT
Irith Pomeranz, Purdue University, US
Salvatore Pontarelli, University of Rome "Tor Vergata", IT
Paul Pop, Technical University of Denmark, DK
Jean-Michel Portal, IM2NP, FR
Laura Pozzi, University of Lugano, CH
Paolo Prinetto, Politecnico di Torino, IT
Mihalis Psarakis, University of Piraeus, GR
Xiaoke Qin, NVIDIA, US
Martin Radetzki, Institut für Technische Informatik, DE

Wenjing Rao, University of Illinois at Chicago, US
Joaquín Recas, Computense University of Madrid, ES
Francesco Regazzoni, AlaRI, CH
Felix Reimann, Audi, DE
Jan Reineke, Informatik, Universität des Saarlandes, DE
Michel Renovell, LIRMM, FR
Pedro Reviriego, Universidad Antonio de Nebrija, ES
Francisco Rincon, EPFL, CH
Tanguy Risset, Insa-Lyon, FR
Rosa Rodriguez, UPC, ES
Jaijeet Roychowdhury, UC Berkeley, US
Antonio Rubio, Universitat Politècnica de Catalunya (UPC), ES
Jack Sampson, Pennsylvania State University, US
Pablo Sanchez, University of Cantabria, ES
Chiara Sandionigi, CEA, FR
Sergio Saponara, University of Pisa, IT
Toshinori Sato, Fukuoka University, JP
Patrick Schaumont, Virginia Tech, US
Gunar Schirner, Northeastern University, US
Julien Schmaltz, Eindhoven University of Technology, NL
Christoph Scholl, University Freiburg, DE
Donatella Sciuto, Politecnico di Milano, It
Alper Sen, Bogazici University, TR
Yiyu Shi, U. of Missouri / University of Notre Dame, US
Donghwa Shin, Yeungnam University, KR
Aviral Shrivastava, Arizona State University, US
Cristina Silvano, Politecnico di Milano, IT
L. Miguel Silveira, INESC ID/IST - Cadence Research Labs, PT
Tajana Simunic Rosing, UCSD, US
Ozgur Sinanoglu, NYU-AD University, AE
Anna Slobodova, Centaur Technology, US
Hayden So Kwok-Hay, University of Hong Kong, HK
matteo sonza reorda, politecnico di torino - DAUIN, IT
Ioannis Soudis, Chalmers University of Technology, SE
Todor Stefanov, Leiden University, NL
Andreas Steininger, Vienna University of Technology, AT
Daryl Stewart, ARM, GB
Haralampus-G. Stratigopoulos, Sorbonne Universités, UPMC Univ.
Paris 6, CNRS, LIP6, FR
Sander Stuijk, Eindhoven University of Technology, NL
Cliff Sze, IBM Austin Research Laboratory, US
Mehdi Tahoori, KIT, DE
Sheldon Tan, UC Riverside, US
Theocharis Theocharides, University of Cyprus, CY
Lothar Thiele, ETH Zurich, CH

David Thomas, Imperial College London, GB
Yvain Thonnart, CEA, LETI, MINATEC, FR
Aida Todri-Sanial, CNRS - LIRMM, FR
Hiroyuki Tomiyama, Ritsumeikan University, JP
Stavros Tripakis, Aalto University/ UC Berkeley, FI
Antonino Tumeo, Pacific Northwest National Laboratory, US
Andy M. Tyrrell, University of York, GB
Gerd Vandersteen, Vrije Universiteit Brussel, BE
Elena Vatajelu, Politecnico de Torino, IT
Ingrid Verbauwheide, KU Leuven and UCLA, BE
Bart Vermeulen, NXP Semiconductors, NL
Tiziano Villa, University of Verona, IT
Eugenio Villar, University of Cantabria, ES

Wensi Wang, Tyndall National Institute, IR
Li Wang, UC Santa Barbara, US
Yu Wang, Tsinghua University, CN
Walter Weber, Namlab gGmbH, DE
Markus Wedler, Synopsys, DE
Norbert Wehn, TU Kaiserslautern, DE
Christian Weis, University of Kaiserslautern, DE
Xiaoqing Wen, Kyushu Institute of Technology, JP
Marc Witteman, Riscure, NL
Stephan Wong, TU Delft, NL
Hans-Joachim Wunderlich, University of Stuttgart, DE
Yuan Xie, Penn State University, US
Jingling Xue, University of New South Wales, AU
Jason Xue Chun, City University of Hong Kong, HK
Sotirios Xydis, National Technical University of Athens, GR
Chia-Lin Yang, National Taiwan University, TW
Wang Yi, Uppsala University, SE
Hao Yu, Nanyang Technological University, SG
Daniel Ziener, FAU Erlangen, DE
Vladimir Zolotov, IBM T.J. Watson Research Center, US