

Technical programme topic chairs

D1 System Specifications, Models, and Methodologies

Chair: Christian Haubelt,
University of Rostock, DE
Co-Chair: Andy Pimentel,
University of Amsterdam, NL

D2 System Design, Synthesis and Optimization

Chair: Luciano Lavagno,
Politecnico di Torino, IT
Co-Chair: Frédéric Pétrot,
TIMA Laboratory, FR

D3 Simulation and Validation

Chair: Mark Zwolinski,
University of Southampton, UK
Co-Chair: Prabhat Mishra,
University of Florida, US

D4 Design for power, variability and aging

Chair: Domenik Helms,
OFFIS, DE
Co-Chair: Marisa Lopez-Vallejo,
UPM, ES

D5 Power Estimation and Optimization

Chair: Jian-Jia Chen,
KIT, DE
Co-Chair: William Fornaciari,
Politecnico di Milano, IT

D6 Emerging Technologies, Systems and Applications

Chair: Siddharth Garg,
University of Waterloo, CA
Co-Chair: Michael Niemier,
University Of Notre Dame, US

D7 Formal Methods and Verification

Chair: Jason Baumgartner,
IBM Corporation, US
Co-Chair: Julien Schmaltz,
Open University, NL

D8 Network on Chip

Chair: Federico Angiolini,
iNoCs, CH
Co-Chair: Fabien Clermidy,
CEA-LETI, FR

D9 Architectural and Microarchitectural Design

Chair: Tulika Mitra,
National University of Singapore, SG
Co-Chair: Todd Austin,
University of Michigan, US

D10 High Level Synthesis

Chair: Ryan Kastner,
University of California San Diego, US
Co-Chair: Lars Bauer,
KIT, DE

D11 Reconfigurable Computing

Chair: Fadi Kurdahi,
University of California at Irvine, US
Co-Chair: Marco Platzner,
University of Paderborn, DE

D12 Logic Synthesis and Timing Analysis

Chair: José Monteiro,
INESC-ID / IST, TU Lisbon, PT
Co-Chair: Valentina Ciriani,
University of Milano, IT

D13 Physical Synthesis and Verification

Chair: Ralph Otten,
TU Eindhoven, NL
Co-Chair: Patrick Groeneveld,
Synopsys, US

D14 Analog and Mixed-Signal Systems and Circuits

Chair: Catherine Dehollain,
EPFL, CH
Co-Chair: Günhan Dündar,
Boğaziçi University, TR

D15 Modeling and Optimization of Interconnect, TSV and Power Grids

Chair: Stefano Grivet-Talocia,
Politecnico di Torino, IT
Co-Chair: Luca Daniel,
Massachusetts Institute of Technology, US

A1 Green Computing Systems

Chair: Ayse Coskun,
Boston University, US
Co-Chair: Martino Ruggiero,
University of Bologna, IT

A2 Communication, Consumer and Multimedia Systems

Chair: Theocharis Theocharides,
University of Cyprus, CY
Co-Chair: Sergio Saponara,
University of Pisa, IT

A3 Automotive Systems and Smart Energy Systems

Chair: Davide Brunelli,
University of Trento, IT
Co-Chair: Bart Vermeulen,
NXP Semiconductors, NL

A4 Ambient Intelligence and Ultra-Low Power for Healthcare and Wellness

Chair: Srinivasan Murali,
SmartCardia, CH
Co-Chair: Elisabetta Farella,
University of Bologna, IT

A5 Secure Systems

Chair: Guido Bertoni,
STMicroelectronics, IT
Co-Chair: Patrick Schaumont,
Virginia Tech, US

A6 Reliable, Reconfigurable Systems

Chair: Jose Ayala,
Complutense University of Madrid, ES
Co-Chair: Marco D. Santambrogio,
Politecnico di Milano, IT

A7 Industrial Experiences Brief Papers

Chair: Emil Matus,
Technische Universität Dresden, DE
Co-Chair: Roberto Zafalon,
STMicroelectronics, IT

T1 Defects, Faults, Variability and Reliability Analysis and Modeling

Chair: Robert Aitken,
ARM, US
Co-Chair: Joan Figueras,
Universitat Politècnica Catalunya, ES

T2 Test Generation, Simulation, Diagnosis and System Test

Chair: Grzegorz Mrugalski,
Mentor Graphics Poland, PL
Co-Chair: Bernd Becker,
University of Freiburg, DE

T3 Test for Mixed-Signal, Analog, RF, MEMS/bioMEMS/MOEMS

Chair: Haralampos Stratigopoulos,
TIMA Laboratory, FR
Co-Chair: Andre Ivanov,
UBC, CA

T4 Design-for-Test, Test Compression, Test Access

Chair: Rohit Kapur,
Synopsys, US
Co-Chair: Paolo PRINETTO,
Politecnico di Torino, IT

T5 On-Line Test, Fault Tolerance and Reliable System Design

Chair: Lorena Anghel,
TIMA, FR
Co-Chair: Fabrizio Lombardi,
Northeastern University, US

E1 Real-time, Networked, and Dependable Systems

Chair: Giuseppe Lipari,
ENS-Cachan, FR
Co-Chair: Stefan M. Petters,
CISTER-ISEP, IPP, PT

E2 Compilation and Code Generation for Embedded Software

Chair: Heiko Falk,
Ulm University, DE
Co-Chair: Florence Maraninchi,
Grenoble INP / Ensimag & VERIMAG, FR

E3 Model-based Design and Verification for Embedded Systems

Chair: Wang Yi,
Uppsala University, SE
Co-Chair: Saddek Ben Salem,
Verimag, FR

E4 Embedded Software Architectures

Chair: Oliver Bringmann,
FZI Forschungszentrum Informatik, DE
Co-Chair: Marc Geilen,
TU Eindhoven, NL

E5 Cyber-Physical Systems

Chair: Anuradha Annaswamy,
MIT, US
Co-Chair: Rolf Ernst,
TU Braunschweig, DE