University Booth at DATE09

Call for Demonstrations – A4 half-page abstract for the 19th of January

As part of the DATE09 Conference, a University Booth will be organised for EDA software and hardware demonstrations. Being hosted by the largest electronic system design and test conference in Europe, the University Booth offers a unique chance to exhibit your prototypes to both Academia and Industry. The University Booth is also suitable to demonstrate the pre-commercial results obtained in government funded projects. Universities, research institutes and government funded project consortia interested in demonstrating their hardware and software demonstrations are invited to fill out and return the application form. All demonstrations will take place on Tuesday 21st, Wednesday 22nd and Thursday 23rd of April 2009 within a dedicated time slot. The Conference and the Exhibition including the University Booth will be held in the Acropolis, in the centre of Nice.

EDA Software Demonstrations

EDA Software Demonstrations can be presented at the booth supported by electronics and other media. Each EDA demonstration should be supported by a poster (WxH: 0.9m x 1.4m). Each demonstrator will get a table to present his demo. There will be a few video-projectors but the computers are the responsibility of the demonstrators.

Working Silicon Hardware Demonstrations

For circuit demonstrations, the same services are available as for EDA software demonstrations (table and poster space). All circuits must be demonstrated within a working environment. Each exhibitor is responsible for bringing and installing the hardware and software resources necessary for showing the entire functionality of the circuit. Each EDA demonstration should be supported by a poster. Each poster should contain a plot of the circuit complemented by any information on the functionality of the circuit, test results and working environment.

Deadline

The deadline for submission is January 19th, 2009. The application form (see next page) + a A4 half-page abstract should be sent by e-mail to both Gilles.jacquemod@unice.fr and Cyril.luxey@unice.fr. The acceptance will be sent back before February 20th, 2009. The two-page final paper is requested for the 6th of March 2009.

Application form for
EDA Software Demos  □

Working Silicon Demos □

At the University Booth

1/ Organisation

Name of the University:
Name of the Institution:
Leader of the Institution:

2/ Contact Person

Name :
Position :
Address :
Country :
Phone :
Fax :
E-mail :

3/ Software name:

or Circuit name :

4/ Topics

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<th>System exploration</th>
<th>Mixed Signal Design and Synthesis</th>
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<td>SoC Platform</td>
<td>Test and Yield solutions</td>
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<td>System C</td>
<td>Hardware Solutions and Test</td>
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<td>High Level synthesis</td>
<td>System Analysis, Simulation and verification</td>
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<td>Design and Tools for FPGA</td>
<td>Others (Give the topic)</td>
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5/ Request for video projector: □ Yes □ No

6°) Abstract of the Function of the software or the circuit, test results and working environment (A4 half-page on a separate page). A two page final paper will be asked later if the demo is accepted. This paper will be included in the DATE website and the University Booth CD Booklet which will be distributed to all conference participants.

Gilles Jacquemod & Cyril Luxey
LEAT – CNRS UMR 6071 – University of Nice
DATE09 University Booth Chairs